Monolithic Interconnected Modules (MIM) for High Irradiance Photovoltaic Energy Conversion: a Comprehensive Review

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Abstract

Monolithic Interconnected Modules (MIM) are densely packed arrays of series interconnected photovoltaic (PV) cells that are manufactured on the same semiconductor substrate. This review presents the result of a prospective study whose objective is to provide an overview of the historical development and current state of the art of the MIM technology. The most outstanding works from the conception of the first MIM devices in the late 70s to the most recent ideas to date, including all relevant milestones achieved during these four decades, are reported. This review focuses on MIM devices that are designed for high-irradiance photovoltaic (HIPV) applications, such as concentrator PV (CPV), thermophotovoltaics (TPV), and laser power conversion (LPC), in which the highly dense series interconnection is particularly relevant in order to boost the output voltage without scarifying the receptor photoactive area.
### List of Acronyms

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<th>Acronym</th>
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<tr>
<td>BPC</td>
<td>Back Point Contact</td>
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<tr>
<td>BSR</td>
<td>Back Surface Reflector</td>
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<tr>
<td>CID</td>
<td>Cell Isolation Diode</td>
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<td>CLEFT</td>
<td>Cleavage of Lateral Epitaxy For Transfer</td>
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<td>CPV</td>
<td>Concentrator Photovoltaic</td>
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<td>DH</td>
<td>Double Heterostructure</td>
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<td>ELO</td>
<td>Epitaxial Lift Off</td>
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<td>EMVJ</td>
<td>Etched Multiple Vertical Junction</td>
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<td>FF</td>
<td>Fill Factor</td>
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<td>GFI</td>
<td>Grid Finger Interconnect</td>
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<td>GPHS</td>
<td>General Purpose Heat Source</td>
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<td>HCPV</td>
<td>High Concentrator Photovoltaic</td>
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<td>HIPV</td>
<td>High Irradiance Photovoltaic</td>
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<td>HCPVT</td>
<td>High Concentrated Photovoltaic Thermal</td>
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<td>IMF</td>
<td>Interfacial Misfit</td>
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<tr>
<td>$J_{SC}$</td>
<td>Short-Circuit Current Density (A/cm$^2$)</td>
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<td>LED</td>
<td>Light Emitting Diode</td>
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<td>LCL</td>
<td>Lateral Conduction Layer</td>
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<td>MBE</td>
<td>Molecular Beam Epitaxy</td>
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<td>MEMS</td>
<td>Microelectromechanical systems</td>
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<td>MIM</td>
<td>Monolithic Interconnected Module</td>
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<td>MJSC</td>
<td>Multijunction Solar Cell</td>
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<td>Acronym</td>
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<tr>
<td>MOVPE</td>
<td>Metal Organic Vapor Phase Epitaxy</td>
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<td>PECVD</td>
<td>Plasma Enhanced Chemical Vapor Deposition</td>
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<td>PV</td>
<td>Photovoltaic</td>
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<tr>
<td>QE</td>
<td>Quantum Efficiency</td>
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<tr>
<td>RIE-ICP</td>
<td>Reactive Ion Etching in Inductively Coupled Plasma</td>
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<tr>
<td>RTG</td>
<td>Radioisotope Thermoelectric Generator</td>
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<td>RTPV</td>
<td>Radioisotope Thermophotovoltaic</td>
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<tr>
<td>SI</td>
<td>Semi Insulating</td>
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<td>SOE</td>
<td>Secondary Optical Element</td>
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<td>SOI</td>
<td>Semiconductor On Insulator</td>
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<td>TPV</td>
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<td>$V_{OC}$</td>
<td>Open-Circuit Voltage (V)</td>
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1. Introduction

Monolithic Interconnected Modules (MIMs) are densely packed arrays of series interconnected photovoltaic (PV) cells that share the same semiconductor substrate. These devices have been extensively developed during the past few decades for high irradiance PV (HIPV) applications, which are characterized by a significantly high irradiance impinging on the PV converter. The most remarkable examples of HIPVs include concentrator PV (CPV) [1-3], thermophotovoltaics (TPV) [4, 5], and laser-power-conversion (LPC) [6, 7]. In these applications, the PV cell is illuminated by irradiances that typically exceed several tens of W/cm², i.e. 100’s times the solar irradiance on Earth. Under these conditions, the PV cells produce extremely high current densities (J), and the fabrication of densely packed arrays of small PV cells becomes necessary in order to reduce (increase) the output current (voltage) of the module.

Several concepts have been developed to manufacture densely packed arrays comprising spare PV cells [8-13], most of them involving a relatively complex assembling technique. MIMs provide an effective alternative to these concepts, enabling a significant reduction of the module area devoted to cell interconnection; and notably simplifying the assembly of the full PV receptor.

In this article we review the historical development of MIMs, providing a compilation of references on this topic and summarizing the different technological options that have been developed during the last four decades. The purposes of this review are to overview and classify the different types of MIMs, to establish the current state of the art, and to identify the most promising future developments.

In section 2, we summarize the main kinds of MIMs, introducing the main concepts related with this technology. Sections 3, 4, 5, and 6 describe the specific developments of MIMs based on Si, GaAs, InP, and GaSb, respectively. Section 7 describes those activities on MIMs comprising III-V based multijunction subcells and finally, section 8 provides a compilation of the most remarkable works along with a description of some possible developments envisaged for the future.

2. MIM technology overview

In an attempt to classify the different MIM technologies, in this article we pay attention to three of their main differentiating features: the semiconductor compound, the electrical isolation technique, and the final application.

Attending to the application, we found three main types of MIMs: those used in CPV, TPV or LPC environments. Regarding CPV, MIMs have been developed for dish/tower CPV power
generation systems, where the sunlight is concentrated by a factor as high as several hundreds on highly efficient, actively cooled PV arrays of relevant size of a few tens of cm$^2$. Dish/tower-CPV systems are an alternative to conventional CPV, with the difference of a single concentrated light spot per systems instead of multiple concentrated spots, each of them illuminating individual cells. The large PV receptor area of dish/tower CPVs makes MIMs perfectly suited for this application, to minimize the area of the receptor dedicated to the interconnection between cells. In TPV applications, the diffuse radiant heat originating from an incandescent source is converted into electricity by infrared-sensitive PV cells. In this case, the irradiance strongly depends on the source temperature and emissivity, ranging from $\sim 10$ W/cm$^2$ for 1000 ºC to $\sim 100$ W/cm$^2$ at 2000 ºC. Due to the nature of the irradiation, TPV applications are typically extended to large areas, where the use of MIMs enables higher collection efficiency and output voltage. Finally, LPCs makes wireless power transfer from the generator to remote loads possible (e.g. satellites, dangerous environments, etc.) by using a laser power source. LPCs convert laser power into electricity at very high efficiencies (>50%) and may operate in the broad range from $\sim 1$ to $\sim 200$ W/cm$^2$, depending on the laser power output. In this case, MIMs enable to boost the output voltage avoiding the use of DC/DC converters. Another possible field of application for HIPV MIMs is microelectromechanical systems (MEMS), which require a high voltage DC power supply. In this case, MIMs enable the miniaturization of high voltage, chip-integrated power supplies.

Attending to the semiconductor compound, we find two main categories: the ones based on Si and the ones based on III-V semiconductor substrates. Si substrates were mostly used during the early times of MIM development technology and they were typically devoted to solar applications. However, the advent of III-V semiconductors in the early 80’s rapidly impacted the MIM development and most subsequent works focused on III-V-based devices in order to take advantage of their greater versatility regarding ternary and quaternary alloying and the availability of different substrates. Among the III-V options, only three substrates have been used so far to fabricate MIMs: GaAs, InP, and GaSb. A number of compounds have been grown by molecular beam epitaxy (MBE), metal organic vapor phase epitaxy (MOVPE), and liquid phase epitaxy (LPE), to produce different types of III-V-based MIMs. For instance, GaAs substrates have been used to fabricate (In)GaAs and GaInP devices, suitable for most visible and near-infrared applications, e.g. most CPV and some LPCs. In applications requiring spectral response at longer wavelengths, InP and GaSb have been used to grow different kinds of compounds with bandgaps ranging from 0.5 to 0.75 eV, such as InGaAsSb on GaSb (used in TPV applications) and InGaAs on InP (widely used in TPV and LPC applications).

Attending to the electrical isolation of MIMs, we find three main categories: those based on semi-insulating (SI) substrates, those using cell-isolation diodes (CID), and those based on
semiconductor-on-insulator (SOI) substrates. The most robust and simple solution is the use of SI substrates, which are manufactured by incorporating certain defects in the semiconductor lattice that cause the Fermi level to be pinned close to the center of the bandgap, emulating the case of a perfectly intrinsic (highly resistive) semiconductor. In this case, the electrical insulation between subcells is readily accomplished by means of wells etched down to the substrate. These wells are covered by an insulating layer to protect the exposed perimeter area of the subcells, allowing the deposition of metals to series-connect adjacent subcells without short-circuiting (Figure 1). The use of SI substrates is the most common choice for manufacturing MIMs on GaAs and InP substrates, for which commercial SI substrates are readily available. Unfortunately, no high quality SI substrate exists on Si or GaSb. Thus, they require other strategies such as the integration of CIDs to create a reverse polarity of the substrate with respect to the subcell base that electrically isolate adjacent subcells. One crucial limitation of this design concerns the CID reverse-bias operation: if the CID is illuminated, there will be a current path through the CID that could short-circuit the corresponding subcell. Besides, there is a limit in the number of subcells that can be series-connected corresponding to the CID breakdown voltage. Other works used SOI substrates, which are manufactured by bonding the active semiconductor layers to an insulating holding substrate, such as glass or ceramics. In this case, the electrical isolation is performed similarly to the case of SI substrates. This strategy typically involves some kind of epitaxial lift off (ELO) process to transfer the very thin active epitaxial layers to the final substrate.

3. **Si-based MIMs**

One of the first methods to fabricate Si MIM devices was patented by Riel in 1970 [14] and utilized the CID strategy (Figure 2-a). In order to mitigate the possible short-circuits caused by illuminated CID, their minority carrier lifetime had to be reduced below 10 ns and they had to use very thick layers, at least twice the diffusion length, as proposed by Chiang et al. in 1979 [15].

In a series of publications dated from 1976 to 1983 [16-21], Warner proposed two alternatives to the designs by Riel and Chiang based on the use of SOI substrates. One of the Warner’s designs (Figure 2-b) used doping wells for the lateral isolation of the subcells. For producing these wells by conventional diffusion doping processes it was necessary to polish the Si wafer until reaching a thickness of ~30 µm. Although still based on CID isolation, in this design, the use of CID was restricted to the adjacent subcells, thus mitigating illuminated CID issues because, under this configuration, they are shadowed by the metallic grid. In 1979, Wada et al. [22] proposed an alternative to Warner’s designs in which lateral isolation was carried out by doping from the two sides of the wafer simultaneously. This enabled very deep isolation wells
and consequently avoided the requirement of very thin wafers on SOI substrates, at the expenses of requiring a more complex photolithographic process. Later on, Warner proposed a simpler approach for selective deep diffusion using an Al thermomigration technique [18]. In spite of the improvements with respect to the original design by Warner, all the above described designs still require reverse junction electrical isolation, with the associated shunting risks.

A second design proposed by Warner consisted on creating isolation baskets or wide grooves, the so-called etched multiple vertical junction (EMVJ), in which the active device was confined (Figure 2-c). A similar approach was proposed by Kaplow and Frank, in a journal article [23] and in a series of patents dated from 1978 to 1979 [24-27]. This kind of devices were fabricated and tested under concentrated sunlight (300 suns) to reach conversion efficiencies above 19% [23]. A similar concept was proposed by Chappell [28] (Figure 2-d) except for using substantially different processing steps that involved ionic implantation and anisotropic chemical etching. At the end, solar MIMs were fabricated using this concept, demonstrating efficiencies of 12.2% under illuminations of 300 suns [28]. In both designs, efficiencies above 20% were expected after some minor technological improvements.

Similar ideas were proposed by Moddle [29], Sinton [30] and Kurt [31] in 1986, 1992 and 1995 respectively. Moreover, the Company AstroPower Inc. presented from 1993 to 2003 [32-34] results of MIMs for space power applications by using a design with similar characteristics to that of Warner [21], Chapell [28] and Kaplow and Frank [23-27], which demonstrated experimental efficiencies of 11.1% at 1-sun illumination [33]. In 2003, Peters et al. [35] presented efficiencies of 11% for MIM devices on SOI substrates using the aforementioned groove-like isolation. In 2005, using a similar design (Figure 2-e), Bermejo [36, 37] and Hammud [38] presented Si MIMs with efficiencies of 4% and 10.5%, respectively.

Besides the already discussed alternatives using CID and SOI insulation techniques, there are works proposing the use of SI semiconductor substrates. One of the first references is that of Routh et al. [39] from 1979. Routh proposed two alternatives: the use of insulating substrates, such as sapphire or highly resistive Si, where the insulation was achieved by means of wells etched all through the substrate, such as in Figure 2-e. In 1982, Evans et al. [40] proposed a similar design in which a highly resistive Si instead of SOI is used. In this case, grooves do not necessarily have to penetrate all through the semiconductor substrate. The use of highly resistive Si substrates was further developed by Valco, Kapor and Evans [41, 42] using a planar technology arrangement (Figure 2-f). In this case, it was observed that insulation was only effective for concentration levels of at least 5 suns. This is because at such high concentration, the current contribution through the shunt resistances is insignificant.
In 1982, Green proposed a design that included an integrated bypass diode for each subcell [43]. In this case, the insulation was obtained by relatively large distance and highly resistive semiconductor regions. That same year, Goetzberger [44] proposed an improved insulation scheme by adding grooves to Green’s design.

In 1990, Swanson [45, 46] proposed an insulation scheme based on back point contact (BPC) solar cells using shunted p/n junctions in the perimeter of each cell as minority carrier traps. The device also included bypass diodes integrated in the structure [45]. A first prototype based on the BPC technology was fabricated using a 6x6 cm² monolithic PV module that integrates two parallel strings of 5 series-connected BPC, producing 140 W under an illumination of 200 suns [47]. The so-called wafer scale integrated (WSI) module of BPC cells is designed for central receiver or concentrating dish applications. The record efficiency of this technology measured under standard conditions (25 ºC solar cell temperature) is 22%, obtained at 58 suns. Regarding other implementations of this MIM technology, a 10 kW reflective dish CPV system was presented by Tilford et al. [48] and co-authored by Swanson and co-workers from SunPower Corporation. The CPV receiver consisted of 10 series/parallel connected cells on a single Si wafer, mounted on a water-cooled plate working at 250 suns. The CPV system was based on an 8.5 m focal distance concentrator dish-mirror. The measured system efficiency of a smaller prototype (1/3-scaled of the original) was 13%. However, 16% system efficiency and 23.5% module efficiency were expected using smaller 26% efficient concentrator cells.

In 2000, Fath and Keller [49, 50] combined highly resistive Si with groove-insulation. In [50] they presented an elaborate theoretical analysis on monolithic interconnection using a highly resistive semiconductor substrate. A year later, Kerst et al., from the Technical University of Berlin, presented a practical implementation of a similar double isolation scheme using thin-film crystalline Si. The experimental prototype consisted of a MIM composed of 20 series-connected solar cells fabricated using ion implantation and photolithography processes on a lightly B doped (10¹⁵ cm⁻³) SOI-wafer with a 15 µm thick active layer [51]. The irradiation with an AM1.5G spectrum light showed an open-circuit voltage (V_{OC}) of 7.5 V and a J above 17 mA/cm².

The Toyota Technological Institute from Japan has recently proposed a 3D interconnection consisting of wiring across vertical sidewalls of Si PV cells. This novel device processing technique combines the so-called photoresist spray coating and the angled exposure [52, 53] as an alternative to spin coating for the deposition of photoresist patterns in semiconductor devices. The angled exposure and the photoresist spray coating, conversely to standard normal exposure, allows patterning vertical sidewalls, which in turn increases the device processing versatility and enables the possibility of designing more complex monolithic multiple-cell
architectures. A number of MIM prototypes with different matrix-like configuration have been fabricated with this technique, reaching relatively high voltages and small rise-up times, suitable for integrated circuit and LPC applications.

Regarding MEMS power supply, where high voltage, rather than high current, power or efficiency are required, several solutions have been proposed based on PV solar cells implemented using MIM architectures. The latter are based on SOI substrates and processed using anisotropic etching of the whole active layer. The research group led by Castañer, from the Technical University of Catalonia, has developed a number of MIM devices intended for powering MEMS. Up to 169 cells with an individual $V_{OC}$ as high as 660 mV have been monolithically interconnected in a 0.42 cm$^2$ module [54], yielding a $V_{OC}$ of 103 V. The device is fabricated by means of a photolithographic process that achieves both low surface recombination and high bulk lifetimes. Cell interconnection is carried out through the front side of the wafer, following a design similar to that of Figure 2-e, in which a buried oxide is used to isolate the active layers of the device. In another work from this same group [55] a micro-battery is assembled to the module for low power-high voltage implantable applications: they exhibit a $V_{oc}$ of 99 V and a power density of 4.8 mW/cm$^2$ [56].

Keeping track of solar cells applied to MEMS power supply, a process based on SOI substrates has been proposed for the integration of power sources, circuits and MEMS within a single monolithic chip, thus, significantly reducing the size of the device. Bellew et al., from the University of California at Berkeley, explains that such integration comprises four major stages: 1) SOI substrate fabrication, in which two oxidized Si wafers are bonded together and polished from one side to obtain the desired thickness, 2) formation of the isolation trenches by means of nitride lining and polysilicon filling, 3) solar cell and circuit fabrication and, 4) etching and release of the MEMS structures [57]. Thanks to this integrated fabrication process, individual cells with efficiencies above 14% and high voltage arrays with up to 200 monolithically interconnected solar cells have been manufactured, which produce output voltages above 88 V.

This Si-based MIMs section cannot end without mentioning the alternative that exists with respect to crystalline Si: micrometric size hydrogenated amorphous Si (a-Si:H) devices have also been proposed in a multijunction configuration and interconnected within the same crystal [58] as a solution to supply MEMS. a-Si:H cells degrade under normal operation, but especially during the first 48 hours, which is evidenced by a significant efficiency drop. However, the output voltage of the cell is reasonably stable, which is the most important criterion for electrostatic MEMS as the operating current is low. In the work by J.B. Lee et al., an a-Si:H solar cell array formed by triple p-i-n/p-i-n/p-i-n multijunction solar cell (MJSC) connecting
100 of such structures in a 1 cm² total device area is proposed in order to produce a $V_{OC}$ of 150 V.

Special mention is obliged regarding the original work by Gover and Stella in 1972, where a vertical multijunction concept is presented [59] meant to prevent a pronounced reduction of the collection efficiency caused by short minority carrier diffusion lengths. This configuration strictly fulfils the definition of a MIM, although its purpose is different than the ones considered in this work. Further works based on vertical junction concepts implemented by means of stacked wafers [60-62] are hence, out of the scope of this review.

4. GaAs-based MIMs

The first work in which III-V materials were used to manufacture MIMs was signed by Borden [63, 64] in 1979, working at Varian Associates (Palo Alto, California). Previous works [14, 15, 19-28, 39] had always employed c-Si substrates and manufacturing processes that could not be extrapolated to III-V materials. This was mainly due to the fact that III-V material processing cannot rely on a native thermal oxide mask as it is the case of the SiO₂ used in Si. Besides, impurity diffusion processes take place above 700 °C in most III-V materials, making them useless once they have been subject to such high processing temperature. In previous works, the fabrication of the p-n junction could be made after photolithography patterning using SiO₂ masking during the diffusion processes, which is not an option in III-V technology.

Borden suggested a way to process epitaxial semiconductor devices such as III-V MIMs. These processing techniques were the same as those developed for conventional III-V cells (without monolithic interconnection), allowing the separation of the specific MIM processing steps from the epitaxial growth of the semiconductor structure. This manufacturing process modularity enables the integration of a MIM production line into an existing conventional III-V cell production line. Similarly, advances in the technology of conventional III-V solar cells could be directly incorporated into the MIM technology, both in terms of efficiency and cost. Furthermore, the process proposed by Borden did not include high temperature processing steps that may modify the epitaxial structure and therefore the device active layers remain unaffected during the processing.

The main restriction from the process by Borden is the need to grow the semiconductor structure on a SI substrate. Borden manufactured MIM structures such as the ones shown in Figure 3-a and Figure 4-b, from epitaxial stacks based on AlGaAs on GaAs, creating grooves that penetrated to the SI substrate to achieve electrical isolation. Thereafter, a SiO₂ insulating layer was deposited in order to protect the exposed area of the p-n junction, allowing the deposition of a metallization to series-connecting adjacent cells without short-circuiting the
junction. It should be noted that SI GaAs substrates have better electrical insulating properties than those of SI-Si, e.g. the ones from [39-45, 49, 50].

The results presented by Borden [63] showed lower efficiencies than those obtained for conventional cells with the same semiconductor structure ($\eta=13.8\%$). The latter can be attributed to two main reasons, being the high series resistance the most feasible one. This resistance came from a poor contact between the Au-Ge/Au metallization and the p-type semiconductor, but also probably to the fact that the current photogenerated throughout the device had to be laterally transported towards the base in order to be collected, which introduced a highly resistive path. The other reason for the low efficiency is attributed to the large area occupied by the interconnection, which included isolation grooves representing ~20% of the total area, to be compared to a maximum of 10% shading factor found on conventional concentrator cells. Regarding the latter, Borden argued that much thinner isolation channels could be produced. However, by that time, the main limitation came from the photolithographic process that defined the insulating SiO$_2$ layers within the channel, because this process does not allow direct contact between the photolithography mask and the bottom of the channel, which leads to a reduction of the spatial resolution of the SiO$_2$ patterns. Borden predicted that such problems would appear when trying to make insulation channels below 50 $\mu$m.

It is worth mentioning that in 1981, Masu et al. [65] proposed a number of alternatives to the process by Borden, trying to avoid short-circuiting risks appearing on the subcell perimeter (where, according to the design by Borden, the p-n junction could be short-circuited due to insulation layer defects). Masu proposed two different solutions aimed to prevent the p-n junction to be exposed on the device perimeter: the use of Schottky-barrier cells and p-n structures where the emitter is extended throughout the whole perimeter (Figure 3-b). To our knowledge, no actual devices have been fabricated so far using this design.

In 1990, McClelland et al., from Kopin Corp., presented experimental results on thin-film GaAs MIM devices that were fabricated using the CLEFT (cleavage of lateral epitaxy for transfer) process [66, 67], which is used for mechanical lift-off of very thin semiconductor layers (Figure 5). Epitaxial growth is done selectively by means of a mask that is deposited on the wafer prior to the epitaxial process. Front-side processing is done before the lift-off of the active epitaxial layers. After the separation, the back side is processed and the different subcells are isolated by means of insulation wells that reach the SI substrate. One important advantage of this design is that the back side contact covers the whole surface; which eliminates the necessity of lateral conduction and provides a considerably lower interconnection electrical conductivity. Two MIMs of 16 cm$^2$ and 2 cm$^2$ with 4 and 8 subcells respectively, were developed using the CLEFT process [67]. These structures demonstrated efficiencies of 21.2% at 1-sun AM1.5G and a very
good fill factor (FF) of 85%. In 1991, Spitzer et al. [68] presented MIM devices for LPC applications that used the same CLEFT processing. These devices were optimized for an equivalent sunlight concentration of 320 suns and had cylindrical geometry with 2, 6 and 12 subcells. They were measured under laser illumination and demonstrated efficiencies above 50%. In 1993, Dingle et al. [69] transferred this technology to the development of light-emitting diodes (LEDs) and fabricated an array of 128 LEDs of 30x15 µm² separated by 60 µm (center-to-center) that was transferred to a Si chip.

In what follows, we describe GaAs-based MIM developments based on Borden’s original design in three particular device applications: LPC, CPV and MEMS.

a) GaAs MIMs for LPC

In 1991 Beaumont et al. [70], based on the design by Borden [1], presented GaAs PV converters for use on remote power supply applications. These devices were circular in order to match the approximate shape of the incident laser light (Figure 4-a). The active area was divided into 6 sectors electrically isolated and series-connected following the scheme by Borden. The base photocurrent flowed laterally until it reached the emitter contact of the adjacent subcell, which, similarly to the design by Borden, could lead to important ohmic losses if the base conductivity was not sufficiently high. Therefore, high base doping was required to reduce the resistivity, which in turn caused a diminution in the minority carrier lifetime and an increase of the free carrier absorption. To this end, Beaumont proposed the insertion of an n⁺ layer under the base in order to improve the lateral current conduction without greatly impacting the photon absorption in the conventional n-type lightly doped base (Figure 1 and Figure 3-c). Since then, this layer has been known as "lateral conduction layer" (LCL). In 1996 Fahrenbruch et al. [71], in collaboration with Photonic Power Systems Inc., presented a work similar to the one by Beaumont where an n⁺-GaAs LCL was employed. Fahrenbruch used "floating bridges" to make the connections, thus avoiding the risk of short-circuits on the subcell perimeter.

Peña and Algora [72-76] from the Technical University of Madrid developed a model to explain the complicated interrelations between different design parameters of a MIM device. The results showed that the most limiting features are: base series resistance (including LCL), perimeter recombination and reflection losses at the isolation grooves. Reducing the area of the device entails less resistive losses in the base, but implies higher optical losses at the isolation grooves and higher perimeter recombination. The efficiency decreases as the number of series-connected sectors and the device area are increased. In these studies, the effect of using a LCL with a higher doping and/or greater thickness was also analyzed [76], leading to an optimum value of 10 µm for the sum of both base and LCL thicknesses. A theoretical work on the influence of the
optical misalignment between a laser light source and the LPC device was also presented in [72, 73], where the trade-off between homogeneity and optical losses caused by ray bundles impinging out of the device were analyzed.

In 2006, Peña and Algora [74, 75] presented experimental results on GaAs MIM devices with a circular geometry of 2 mm diameter using 4 and 6 series-connected sectors. Efficiencies above 43% were measured under 810 nm laser illumination for laser powers of about 0.3 W (9.5 W/cm²). In [74] the influence of the processing steps on the optoelectronic characteristics is analyzed and two crucial steps are identified: the fabrication of the insulation barriers and the cap-etching. Both are related to the possibility of shunting the base of adjacent sectors, the higher series resistance and the losses associated with the non-uniformity of the incoming illumination. A thorough experimental investigation on the impact of LPC misalignment is carried out in [77] for the evaluation of the passive assembly tolerance of the PV receiver with respect to the incident laser spot. In a subsequent work [78], the same authors developed a refined version of the experimental set-up previously used [77], together with a theoretical model to analyze tolerance requirements of existing MIM LPCs.

In 2002, Krut et al. [79] from Spectrolab Inc. developed a 2-subcell MIM for LPC. In this design (Figure 3-d), an n/p structure is used with an n-type LCL (nnp-MIM) similar to that described by Wilt et al. [80] (see “InP-based MIMs” section). This design is aimed at reducing the lateral series resistance of the emitter and the LCL thanks to the high mobility of the electrons. The drawback is the necessity of a tunnel junction in order to connect the p-base with the n-LCL. In [79] Krut presented circular MIM devices of 1.5 mm in diameter that were tested under 810 nm laser source with input powers in the range of 0.1 to 1 W (5.7-57 W/cm²) to demonstrate conversion efficiencies of 40% for 25 W/cm² laser source.

In 2008, Bett et al. and in 2009, Schubert et al. [81, 82], following the preliminary work by Gover and Stella [59] (see “Si-based MIMs” section) for a MJSC with subcells of the same material, proposed two alternative designs for MIM development: a similar concept to that of Krut [79] (Figure 3-d) with an improved GaInP LCL on top of the emitter, and the monolithic stacking of two GaAs subcells connected by a tunnel junction (Figure 3-e). In this last design, the upper subcell is thinner than the lower one in order to match their currents. In this arrangement, the output voltage will be two times higher than that of an independent GaAs cell.

Valdivia et al., from the University of Ottawa has recently proposed a similar MIM design for use under laser illumination that produces output voltages higher than 5 V [83]. This design comprises up to five monolithic, lattice-matched, vertical stack single-junction GaAs subcells separated by identical tunnel junctions. Such structure resembles a MJSC except that all subcells are made of the same semiconductor material and the photons to be absorbed are
monochromatic. As in [81, 82], each of these junctions has a variable thickness that is function of the photon flux that it is meant to absorb according to the subcell series-connection photocurrent constraint. The device showed a conversion efficiency of 60.1% under 835 nm laser illumination with an equivalent irradiance of 110 suns. Advantageously, the vertically-stacked monolithic configuration eliminates the need for fabricating isolating trenches and further metal interconnections. This provides simpler manufacturing process, increased light collection, photogenerated current and insensitivity to the alignment of the incident spot to lateral or angular errors.

b) GaAs MIMs for CPV

The first reference presenting a MIM device for dish-CPV applications is authored by Ward. et al. [84] in 1998, who directly transferred their previous TPV designs to CPV applications (see “InP-based MIMs” section). In [85] he presented a 4-subcell MIM of 0.25 cm² using an n/p structure with p-type LCL (the same as that of Figure 3-c but using reverse polarity). Although the device was not fully optimized, a high FF of 72% for 390 suns was demonstrated.

In the framework of the HICON-PV project (2004-2006), van Riesen et al. [86, 87] manufactured a 1 cm² GaAs MIM containing 10 subcells using a p/n structure and a 3 µm thick n-type LCL. The measured efficiency was 11.5% at 500 suns. The results revealed series resistance issues caused by the metal contact and shunting due to bad performing perimeter insulation. The use of different polarities for the emitter and the LCL forced to independently optimize the metal contacts for each of these layers, which was not relevant in TPV devices, where low bandgap materials are used. In [86] a Ti/Pd/Au system was used to contact the p-GaAs emitter, while a structure with Ni/AuGe/Ni was used to contact the n-type LCL. The perimeter insulation was carried out by means of 600 nm thick SiN/SiO₂ layers. In subsequent works, this MIM device was further optimized: in [87] the AlGaAs window layer was substituted by GaInP and the insulation was substituted by a polyamide, such as in [75]. The emitter contact was also substituted by a Cr/ZnAu system, in addition to a 3 µm thick silver layer aimed at improving grid conductivity. Thanks to these improvements, efficiencies of 20.8% at 150 X and 17% at 1000 X were measured.

Loeckenhoff et al. [88] proposed a new MIM design based on Wilt’s n/p/n design [80] (Figure 3-d) and with an integrated monolithic bypass diode (Figure 3-f). One of the advantages with respect to previous HICON-PV designs was the possibility of using the same metal combination (Ni/AuGe/Ni) for the two LCLs (LCL1 and LCL2 in Figure 3-f) and the emitter (all of them n-type). The drawback of this design is the larger area dedicated to the insulation wells (about 100 µm). However, still some issues concerning the metal-semiconductor contact and tunnel
junction breakdown were detected in these devices. Loeckenhoff solved these problems [89] by changing the metallization from Ni/AuGe/Ni to Pd/Ge and improving the growth technique of the tunnel junction layers. Besides, a back surface Ti/Au reflector was incorporated to improve the heat management of the device. In [89] it was finally presented a 24 MIM integration on a 10x10 cm² receptor (Figure 6). The highest MIM conversion efficiencies were 22% at 200 X and 19.9% at 962 X. The average efficiency of the 24 MIM devices was 16.7% and the overall module efficiency containing 24 MIMs (6 in parallel and 4 in series, see Figure 6) was 17% at 40 X (no results were presented at higher concentrations). It is remarkable the very high $V_{OC}$ of 110.6 V if compared with the small short-circuit current of 0.8 A, for a FF of 78%.

References on the integration of MIM devices in complete CPV systems can be found in [90] and [91]. A comparative study of different MIM devices developed within HICON-PV can be found in [92]. In [93] Loeckenhoff summarizes the work done on the fabrication of MIM devices and their integration on a complete CPV receiver. In Ref. [94], a 4.33 cm² single-junction GaAs MIM consisting of 23 interconnected cells is attached to a manifold micro-channel heat sink meant to recover and make a profitable use of the thermal energy produced. Such combined high concentrator PV and thermal system is known as high concentrated PV thermal (HCPVT). When thermal power is taken into account, the combined efficiency of the system increases from 15% to 60%. Besides, the performance of the PV part is very little affected by the coolant temperature increase (from 25 °C to 70 °C) required to maximize the exergy efficiency. An equivalent HCPV system was designed including four MIM attached to a secondary optical element (SOE) devoted to increase concentration and homogenization of the irradiance on the cell surface, and the efficiency improved to 63% [95].

Similar developments have been recently carried out at the University of Tokyo, where the use of monolithically integrated bypass diodes adjacent to each subcell without the need for extra LCL [96] have been investigated, as it was originally proposed by Loeckenhoff [88]. In [97], they presented a 50 mm² MIM with 5 p/n subcells and n-type LCL (as in Figure 1 and Figure 3-c) that claim improved mesa profiles and better insulating material than the common polyimide used between mesa sidewalls and electrodes from adjacent subcells. On the one hand, abrupt vertical mesas are obtained using Cl₂ plasma etching and, on the other hand, performing SiO₂ insulating layers deposited by sputtering are used to isolate the MIM segments. A relatively high packed configuration is obtained, with up to 82% effective useful active area: only 9% of the whole MIM area is covered by insulator and another 9% is filled by the metal grid. 1 sun characterization showed MIM efficiencies slightly higher than 15% and a $V_{OC}$ close to 1 V per cell.
c) GaAs MIMs for MEMS

The use of GaAs MIMs has also been reported for the application to MEMS. Small GaAs cells on SI substrates present power loss mechanisms related to the leakage current (shunt resistance), rather than series resistance, as it is usually the case in conventional PV solar cells. The use of III-V materials for the implementation of MIM devices present the advantage of SI substrate availability and the possibility for enabling a higher $V_{oc}$ per cell, i.e. GaAs and related materials have bandgaps that are both greater than that of Si as well as direct, which allows $q\cdot V_{oc}$ closer to the bandgap energy. Furthermore, the III-V solar cell absorption threshold can be easily tuned thanks to the possibility of ternary and quaternary alloying, which is an interesting electronic feature as regards of wavelength selectivity applied to MEMS power supply, i.e. light beams of different wavelengths can be used independently for the activation of the MIM devices.

In [98] a monolithical array with 24 series-connected micro-solar cells is presented for its use in the field of MEMS power supply. The cell array is integrated in a rectangular 0.8 mm$^2$ MIM device fabricated on a SI GaAs substrate and interconnected on the flip side of the substrate via holes. It shows a $V_{oc}$ of 22.5 V under a monochromatic illumination of 815 nm and an irradiance equivalent to approximately 5 suns. The leakage current, which drives the losses of the device, is measured and modeled, being identified as occurring throughout the illuminated surface of the SI substrate. Metallic light-blocking films were deposited in the vicinity of the cells during the process of wiring lead formation in order to suppress photocurrent in the surface region, which in turn prevents further reduction of the substrate sheet resistance and hence, decreases the leakage current.

In a further work, Ohsawa obtains 100 V from a MIM containing 120 cells [99] with a similar concept. The response of the device is analyzed as a function of transient light for its use in MEMS with satisfactory results.

A theoretical work by Y. Wang et al. studies the effect of the MIM leakage current and proposes a modification of the conventional solar cell equivalent circuit model consisting of the addition of a shunt resistor on its terminals [100]. Results from this model show that the quality of the MIM fabricated with GaAs cells on SI substrate (mainly the $V_{oc}$) can be significantly improved through the reduction of the number of cells per line and a relative cell area that maximizes the trade-off between leakage current and perimeter recombination (simulations place this optimum between 80% and 90%).

The same research groups later proposed a 1 cm$^2$ experimental implementation with 108 GaAs cells that produce a $V_{oc}$ of 84 V using a SiO$_2$/TiAu/SiO$_2$ multiple-layer structure in addition to the more common polyimide insulating layer [101]. The MIM provided an efficiency of 11.2%.
In [102], a cell perimeter to area threshold is set for the GaAs micro-cells used in MEMS MIM from which the recombination in the space charge region is dominated by the perimeter surface.

5. **InP-based MIMs**

InP arises as a promising substrate candidate for low-bandgap semiconductors with direct bandgaps in the range of ~0.55-0.74 eV. These low-bandgaps are well suited for TPV, which requires absorbing low energy photons emitted by incandescent bodies. Similarly, a wide family of LPCs employs low-bandgap semiconductors to convert infrared laser light into electric power, matching the transmission windows of fiber optics. Such low-bandgap PV devices could not be readily fabricated on GaAs because no low-bandgap ternary or quaternary alloy exist lattice matched to GaAs and very restricted possibilities exist close to its lattice constant, all of them with bandgap energies very close to 1.4 eV. Most of the existing works on MIMs manufactured using InP substrates are based on InGaAs compounds. This ternaries can be grown lattice-matched to InP with a bandgap of 0.74 eV and lattice-mismatched with bandgap energies as low as 0.55 eV. Other options employ quaternary compounds such as InAlGaAs or InGaAsP meant to lower the absorption threshold and adapt the lattice constant to that of InP. In what follows, the advances proposed by the different works devoted to LPC and TPV using InP-based substrates are described.

a) **InGaAs MIMs for LPC**

The first work in which a monolithic series-connection of cells is carried out using InP substrates was presented by Ng et al. [103] in 1982. In addition, this is the first reference we can find about MIMs applied to remote LPC. The device consists of a GaInAsP/InP heterostructure divided into 12 subcells arranged in a radial configuration. Once the semiconductor structure has been fabricated, the interconnections are made by "wire-bonding" as Borden did in the past [63] as well as other authors [65, 70, 72-76, 79, 82]. Ng obtained efficiencies of ~30% under the illumination of 1.06 µm and 44 mW laser, achieving a FF of 76%.

In 1994, Wojtczuk et al. [104-106], from Spire Corporation, presented PV devices designed to operate with laser light of wavelengths between 1.3 µm and 1.5 µm. This work is the first in which remote LPC were developed in this wavelength range. In [106] results from MIMs based on InGaAs and InAlGaAs (both epitaxially grown on InP) are presented. In both cases, a p-n structure is used with an n-type InGaAs LCL (Figure 3-c). The rationale behind the use of InAlGaAs is the greater versatility to tune the bandgap energy, which in turn, is meant to provide optimized solutions for a given spectrum. However, most results obtained from InGaAs devices were better than the ones obtained from InAlGaAs despite having optimum gap. In
particular, the quantum efficiency (QE) obtained from the ternary was considerably better, because the epitaxial growth of a quaternary material such as InAlGaAs is more complex and is subject to problems related to material quality, atomic ordering, dislocations, etc. Furthermore, Al is easily contaminated with oxygen, creating additional recombination centers. InGaAs MIM devices comprising 8 subcells were illuminated by a solar simulator to produce an irradiance of 4.1 W (5.12 W/cm²) and 0.45 W (0.56 W/cm²). The device showed efficiencies of 16% (FF=51%, V_{OC}=2.98 V, short-circuit current (I_{SC})=0.42 A) and 22% (FF=70%, V_{OC}=3.02 V, I_{SC}=46 mA) respectively. According to [106], high series resistance losses from different components impacted the device performance (front-grid metallization, LCL, emitter, etc.). Other losses related to the processing of the device were pointed out regarding the definition of the dielectric layers used for the isolation grooves, related to difficulties in the photolithographic process on non-planar surfaces, which may lead to the appearance of short-circuits. In [105], InGaAs MIM devices, both InP lattice-matched (bandgap wavelength of 1.65 µm) and lattice-mismatched (bandgap wavelength of 2.2 µm) are presented.

It is worth mentioning that Wojtczuc although focused on LPC applications, presented a theoretical analysis [107] of the advantages of the MIM technology applied to TPV, when this community was beginning to be interested in this technology (in particular Wilt et al. [108]).

b) InGaAs MIMs for TPV

The first reference in which MIMs are used for TPV applications appeared in 1996 and belongs to the joint effort of NASA and Essential Research Inc. (Wilt et al [108]). They combined for the first time two old concepts: Back Surface Reflector (BSR) and monolithic integration. Both concepts accurately fit with the requirements of a TPV system, in which a photon recirculation system is required and in which the power density of the incoming light is very high. In addition, both the inclusion of BSRs and the monolithic integration shared the need for SI substrates, on the one hand to prevent free electron absorption and on the other hand to ease electrical isolation between subcells. The semiconductor structure used in [108] was p over n (p/n) in order to include an LCL under the n-type base (Figure 3-c), taking advantage of the greater electron mobility compared to that of holes. Furthermore, the increase in optical bandgap caused by a high InGaAs n-type doping of the LCL (attributable to the Burstein-Moss shift due to degeneracy of the heavily doped n++ material) allowed photons with energy slightly above the gap that were not absorbed in the base, not to be absorbed in the LCL, being reflected

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1 The incorporation of a reflector on the rear of the cell allows the reflection of photons that are not absorbed by the active semiconductor layers. Such recirculation allows, on the one hand, reducing the requirements for thermal dissipation, and on the other hand, the application of spectral control techniques, returning the unabsorbed photons to the emitter.
in the rear reflector and increasing the probability to be absorbed in the base. This enabled the
design of a narrow base without sacrificing photon absorption\(^2\). A narrow base has three main
advantages: increased \(V_{OC}\), enhanced collection probability and ease of the narrow isolation
channel processing. Another reason for using a p/n structure was the fact that the free electron
absorption is much smaller (3.5 times) in n-InGaAs than in p-InGaAs, so absorption losses
caused by free carriers are reduced because of the much thinner emitter layer compared to the
base.

The devices presented in [108] consist of 8 subcells of 1.2x10 mm\(^2\) each, electrically isolated by
an electronic configuration similar to that introduced by Borden [63], where the insulator
channel was 270 \(\mu\)m wide and used SiO\(_x\) as dielectric insulator. The LCL comprised a thickness
of 2.7 \(\mu\)m. AuGe contacts were used to connect both the p-type emitter and the n-type LCL.
This was not possible in higher bandgap materials, such as GaAs (see "GaAs-based MIMs"
section), for which metal-semiconductor ohmic contacts are more complicated to obtain. The
results showed a \(V_{OC}\) of 0.488 V and a FF of 40.9\% at 1 sun and a \(V_{OC}\) of 2.6 V and a FF of 53\%
at 140 suns. Parallel resistance problems (related to short-circuits in the perimeter of the
subcells) as well as series resistance problems (due to problems with the semiconductor
structure, LCL resistivity and the metal contacts) were identified.

During the next two years, Wilt and Fatemi continued the work begun in [108] and presented
devices incorporating a number of improvements [109, 110], which are fully described in [111].
Different dielectric materials were tested as insulators: Ta\(_2\)O\(_5\), spin-on-glass and Si\(_3\)N\(_4\), being the
latter the one offering best results with resistivities higher than \(10^{10}\) ohm-cm. As to metal
contacts, several options were tested looking for a low specific contact resistance, good
adhesion both with InGaAs (n- and p-type) as well as with insulating materials and a high
reflectivity in the infrared. They identified both Cr-Au and Ti-Au as good alternatives, although
the Cr layer deteriorated the reflection properties, so the BSR metallization was finally made of
Au. These device processing modifications were employed for the manufacture of two types of
semiconductor structures, one of which consisted of an InGaAs device lattice-matched to InP,
constraining the InGaAs bandgap energy to 0.74 eV. The other structure consisted of an InGaAs
material lattice-mismatched to InP in order to obtain a bandgap of 0.55 eV, more suitable for
TPV applications. The first of them showed better performance than the previous work [92],
with a short-circuit current density \((J_{SC})=70 \text{ mA/cm}^2\), \(V_{OC}=3.2\) V and FF=66\%. Conversely, the
0.55 eV device showed very poor results \((J_{SC}=4.8\text{ mA/cm}^2\), \(V_{OC}=0.668\) V and FF=54.6\%)
although no measurements were performed under high concentration.

\(^2\) Long wavelength photons are absorbed by deeper layers of the device and could remain unabsorbed if
the device is not thick enough. Therefore, rear reflectors allow doubling the optical path of non-absorbed
photons in the device, thus increasing the absorption probability.
In [112], Fatemi and Wilt presented new MIM devices, this time with 15 subcells instead of 8, as it had been the case so far. The insulation channel width was reduced from 270 µm to 50 µm and therefore the packing density was significantly increased. The metallization interconnection design in the channel was also improved, changing the continuous metal layer for another one including a metallization grid, thus avoiding short-circuits and maximizing photon absorption in these regions\(^3\) (Figure 4-c). 0.74 eV devices showed FF between 70% and 75% for \(J\) up to 1.5 A/cm\(^2\) and 0.55 eV MIMs reached this time FF between 58%\(^4\) and 62% for \(J\) up to 3.6 A/cm\(^2\).

Almost simultaneously with the designs by Wilt, Ward et al. [85], from the National Renewable Energy Laboratory (NREL), presented a new design for MIM that would be eventually patented [113]. In the conventional MIM design by Wilt, narrow subcells had to be fabricated in order to minimize resistive losses in the LCL. A wider subcell design requires increasing the thickness of the LCL to compensate the larger lateral flow of majoritary carriers, which in turn entails further difficulties regarding the processing of the device (insulation channels become thicker) as well as a deterioration of the photon recirculation process (a wide LCL layer implies a greater absorption by free electrons, which prevents the BSR to redirect unabsorbed photons efficiently). In Ward’s design [85] both a front emitter and a rear LCL metallization grids are interdigitated (Figure 4-d). This way a thinner and less doped LCL could be used, drastically diminishing the absorption by free electrons. This concept was named “Grid Finger Interconnected” (GFI). GFI-MIMs of 1x1 cm\(^2\) containing eight 0.74 eV InGaAs subcells lattice-matched to InP were fabricated, obtaining satisfactory results: FF=72% and \(V_{OC}=3.62\) V for a \(J_{SC}\) of 0.9 A/cm\(^2\).

Fatemi and Wilt decided to compare experimentally the aforementioned two design alternatives [114]. In this work they developed GFI-MIM devices with a 0.25 µm thick LCL and a doping of \(10^{18}\) cm\(^{-3}\) (to be compared to the 1 µm thick and \(10^{19}\) cm\(^{-3}\) doped LCL from the conventional design). Results from different kinds of MIM devices were presented in [114]: employing 0.6 eV InGaAs (lattice-mismatched to InP) according to the conventional and GFI schemes and 0.74 eV (lattice-matched to InP) devices based on the conventional scheme. The results showed slightly better performance for the conventional devices, either in terms of \(J\), \(V_{OC}\), and FF. However, it should be noted that the worse results obtained for the GFI scheme are related, at least in part, to the larger device area (2.54x2.54 cm\(^2\) with respect to 1 cm\(^2\) of the conventional design).

\(^3\) Later on, Ballinger showed that this option implied larger optical losses and therefore, was not adopted in further works.

\(^4\) It is important to note that the photocurrent generated by same illumination conditions is higher in a low bandgap device and the maximum achievable FF decreases as the bandgap energy is further reduced.
In 1998, a series of works analyzed different aspects of the MIM design: Ringel et al. [115] investigated the inclusion of metallic epitaxial layers replacing the LCL to improve the lateral majoritary carrier transport. In addition, these layers played the role of a BSR. Ballinger et al. [116] presented an analysis of the BSR operation within a MIM device based on Monte-Carlo ray-tracing techniques. Several MIM manufacturing recommendations arise from this modeling regarding the reduction of the optical losses and the improvement of the photon recirculation in TPV systems.

A further work by Wilt [80] [117], showed a new MIM device that substantially improved the ones fabricated so far. Previous designs employed p/n structures because the base of the cell had to be electrically connected to a highly conductive n-type LCL. Wilt [80] proposed the use of an n/p structure because n-InGaAs features a much lower free carrier absorption compared to p-InGaAs and also because n-InGaAs majority carrier mobility is 25 times higher than that of p-type InGaAs. The obvious problem is related to the highly resistive p-type LCL that has to be used instead of the more performing n-type one that would produce an n/p/n configuration that includes a reverse p/n junction preventing the flow of current. Therefore, to connect the p-type base and the n++-LCL Wilt proposed the inclusion of a tunnel junction between them (Figure 3-d).

Wilt showed results of 0.55 eV and 0.74 eV InGaAs MIM devices using this new n/p/n configuration and compared them with previous (p/n) designs. He always used the same LCL, so the observed differences could only be attributable to the use of n/p or p/n stacking. Apart from the better electrical performance of the n/p/n structure, a clear improvement of the reflected component of the light in the rear side of the device (due to the BSR) is observed, which is attributed to the low free carriers absorption in the emitter.

Regarding the improvement of the 0.55 to 0.6 eV devices (lattice-mismatched to InP), Wilt analyzed different alternatives to grow a buffer layer capable of adapting the lattice constant of the InP substrate towards that of the InGaAs semiconductor [118]. The solution finally adopted consisted of using a gradual InAsP layer. This layer could also be used as LCL if a sufficiently high doping was applied.

In parallel, Murray [119] from Emcore Photovoltaics, developed several devices based on 0.55 eV InGaAs using the aforementioned n/p/n configuration with a tunnel junction, but using a slightly different epitaxial structure, very similar to the one previously presented by Wanlass (NREL) [120]. It was based on an InAsP/InGaAs double heterostructure (DH) used to reduce minority carrier recombination and including a "step-graded" InAsP layer. As regards of the MIM, the most relevant feature from the design by Wanlass was the use of CID to isolate the cells, which was placed under the DH cell. The CID had a doped n+/p (since the cell was p/n).
The n⁺ region of the CID was used as stop-etching layer so that the insulation channels penetrated precisely until the n⁺ layer, making possible its use as LCL.

In 2008, J. G. Cederberg, from Sandia National Laboratories presented an InAsP step-graded buffer aimed at growing metamorphic In₀.₆₈Ga₀.₃₂As cells in MIM configuration on an InP substrate [121]. He addressed the problem of surface defects acting as shunts by modifying pre-growth conditions and produced TPV cells with a $V_{oc}$ of 348 mV per junction and FF exceeding 70%.

More recently, the team led by S. A. Ringel, from the Ohio State University, developed a metamorphic growth procedure of In₀.₆₉Ga₀.₃₁As cells on SI InP by molecular beam epitaxy (MBE) [122], including a novel In₀.₇Al₀.₃As window layer optimized for an n-on-p configuration that incorporates an n-type InAs₀.₃₂P₀.₆₈ LCL. MIM structures were designed and individual TPV cells were fabricated and measured, achieving a $V_{oc}$ of 0.355 V and a FF of 66.5%. Outstanding internal QE results for short wavelengths revealed the relevance of the In₀.₇Al₀.₃As window layer.

A comprehensive review of InP-based MIMs for TPV applications is presented by Wilt et al. [123]. In [124] a summary of the specific activity of the NREL until 2004 on the manufacture of low-bandgap InGaAs cells on SI InP substrates can be found, including the fabrication of MIMs. From this time on, no new ideas were proposed regarding the single junction MIMs and all the works aimed at the technology transfer to the industry (mainly made by Emcore Photovoltaics) or its application to complete systems. The newest contributions focused on integrating tandem cells in MIM devices. These studies are discussed in the "Multijunction MIMs” section.

In 2004, Wernsman (from Bechtel Bettis Inc.) in collaboration with Emcore Photovoltaics (among others) used 0.6 eV InGaAs MIM devices (2x2 cm² and 30 subcells) in TPV systems, achieving one of the highest TPV efficiencies to date (close to 24%) [125].

Emcore Photovoltaics decided to study the feasibility of making 0.6 eV InGaAs MIMs at large scale [126]. The design employed is identical to the previous one presented by Murray [119], which combined the n/p/n design introduced by Wilt [80] and the epitaxial growth based on InAsP/InGaAs DH by Wanlass [120]. They fabricated MIM devices based on three different design variations using a total of 130 600 µm thick wafers of 76 mm in diameter, purchased to different suppliers. The whole processing required four different photolithography masks, corresponding to the metallization of the rear contact, etching of the isolation channels, definition of the dielectric layers (insulation and anti-reflecting coating) and front-grid metallization. In [126] a detailed description of the fabrication process as well as the equipment
employed is presented, together with results on the emitter contact and LCL resistances. Conversion efficiencies of complete TPV systems employing such devices are also shown in reference [127]. In [128] S.L. Murray (also from Emcore Photovoltaics) presented a study on the device metallization regarding the trade-off between electrical and optical operation.

In 2006, Newman, also from Emcore Photovoltaics, presented a joint study with Bechtel Bettis Inc. and Lockheed Martin Corp. in which the yield of a pilot plant devoted to the manufacture of InP substrates-based MIM devices is analyzed [129]. For this purpose, they produced 0.6 eV InGaAs cells on 166 3-inches SI-InP wafers. Different growth and device manufacturing alternatives are also discussed in this work. 36 MIM devices were fabricated on 9 wafers (each containing 4 2x2 cm² MIMs) with the following average values: \( V_{OC}=10.93 \text{ V} \pm 0.023 \text{ V} \) and \( \text{FF}=68.3\% \pm 0.783\% \) for \( J_{SC}=2.23 \text{ A/cm}^2 \pm 0.002 \text{ A/cm}^2 \). A TPV system average efficiency (defined for a 1000 °C black body) of 20.3\% \pm 0.27\% is obtained from a sample population of 664 MIMs. In 2007, Su et al. [130], from the University of Notre Dame in the U.S., together with Essential Research Inc. and NASA, showed a numerical modeling of the 0.6 eV InGaAs devices manufactured by Emcore Photovoltaics on SI-InP.

InGaAs TPV MIMs were used by NASA during the development of Radioisotope TPV (RTPV) generators since 2003 [131]. RTPV generators are under development at NASA as a high efficient and high specific-power alternative to current state of the art radioisotope thermoelectric generators (RTGs). State of the art multi-mission RTGs produce 125 W_e from a total thermal power of 2 kW_th (efficiency of 6.2\%) which is generated by eight GPHS (general purpose heating system) units containing Pu-238 fuel. RTPV is aimed at producing the same amount of power using only 2 GPHS units, enabling a 5-fold higher specific power of \(-15\) W/kg. The RTPV generator developed by NASA used 0.6 eV InGaAs MIMs produced by Emcore and interference/plasma filters produced by Rugate. A 10x10 cm² TPV array (Figure 6) containing 16 MIMs, each of them comprising 25 series-interconnected InGaAs subcells, was fabricated to provide \(-50\) W_e from a heat source temperature of 1077 °C (electrically heated), demonstrating conversion efficiencies in the range of 18-20\%. Furthermore, neutron radiation tests were carried out to predict a 20\% degradation of the TPV MIM converter by the end of a 14-year mission, which compares to 14\% for the multi-mission RTG system. Furthermore, tests of MIM devices at 90 °C demonstrated no degradation for 4000 hours of operation.

6. **GaSb-based MIMs**

GaSb-based MIM devices have been developed almost exclusively for TPV applications. The benefit of GaSb for TPV relies on the wide range of available semiconductors lattice-matched to GaSb with bandgaps ranging from \(-0.3\) eV to \(-1.6\) eV. As such, it is possible to grow low
bandgap GaSb-lattice matched ternary and quaternary compounds such as InGaSb, InAsSbP, InAsSb or InGaAsSb, as well as wide-bandgap materials such as AlGaAsSb or GaSb for the window layers. Furthermore, by using these materials it is also feasible to manufacture tandem and multijunction TPV cell structures. Unfortunately, the drawback associated with GaSb-based MIM is the lack of GaSb SI-substrates, because of the unavoidable high p-type doping inherent to GaSb. Therefore, the proposed solutions employ other ways of electrical insulation.

One of the alternatives is the use of CID isolation, i.e. reverse p-n junction isolation, as the one patented by Wanlass in [132] and used by Wherer in [133] (see "GaAs-based MIMs" section). This way, the isolation diodes require withstanding the reverse voltage of the whole subcell array that constitutes the MIM, so it is essential that such diodes comprise a high reverse breakdown voltage. Palmisiano et al. [134] from Bechtel Bettis, fabricated InGaAsSb MIMs using AlGaAsSb CIDs with breakdown voltages of 5-7 V. Nevertheless, the resulting 15 series-connected cell MIM devices showed a much degraded output voltage of 0.42 V, given that each cell adds approximately 0.2 V. This poor performance was attributed to insulation problems associated to the series-connection of a large number of subcells within the MIM.

Another alternative for manufacturing MIM devices on GaSb wafers is the transfer of the epitaxial layers into an insulating substrate or a substrate coated with insulating layers (e.g. SOI substrate). Under this approach, the substrate could be further coated with a highly reflective coating playing the role of a back surface reflector, which is relevant for spectral control in TPV applications. In [135-138] Wang, from the Massachusetts Institute of Technology, presented InGaAsSb MIM devices on SiO$_x$/Ti/Au coated GaAs substrates. In this case, the additions of complete subcell voltages within the MIM were demonstrated, i.e. 1.8 V for a 10 subcell MIM, provided that each subcell produces ~0.2 V separately. Anyway, the devices manufactured still required an optimization process to reduce ohmic losses throughout the device.

Very recently, MTPV Corp. and collaborators are exploring the possibility of growing epitaxial GaSb layers on SI-GaAs substrates to manufacture MIM devices [139-141]. These investigations could open the path for manufacturing a number of low bandgap Sb-based MIM devices using the conventional techniques already developed for (In)GaAs compounds, which have already been proved feasible even for industrial scale production.

7. Multijunction MIMs

In the early 80s, Borden, who had already introduced a way to monolithically interconnect GaAs cells in series [63, 64], proposed an alternative to tunnel junctions for use in MJSC architectures. It consisted of etched channels that penetrated to the device regions where the contact was to be made [142, 143]. This processing technique was proposed as a solution to the
inefficient tunnel junctions that were fabricated at that time for the series-interconnection of subcells within a MJSC.

Eventually, high quality tunnel junctions were finally achieved and these etched channels were discarded. Anyway, in further MJSC designs, device architectures similar to the one proposed by Borden were employed in order to enable independent access to the terminals of each subcell. This is the case of the design by Wanlass [144, 145], from NREL, who proposed a 3-terminal solar cell in tandem configuration using this technique. This design allowed the independent external connection of each cell within the tandem device in different arrays, hence avoiding subcell current-matching.

The integration of a MJSC architecture in a MIM device (MJSC-MIM) was firstly proposed by Spectrolab [146] in which the use of CID insulation is proposed in order to isolate the substrate, generally made of Ge, so that GaInP/GaAs/Ge triple-junctions could be implemented in MIM configuration. To our knowledge, no experimental results of such devices have been presented so far.

a) MJSC-MIMs for TPV

In 2002, Wherer and Wanlass [133] respectively from Bechtel Bettis Inc. and NREL, in collaboration with NASA and Emcore, started the development of MIM devices for TPV applications. These devices incorporated a tandem configuration with InGaAs subcells lattice-matched to InP (of 0.74 eV) and another lower bandgap InGaAs cell (of 0.55 eV) with a considerable lattice-mismatch with respect to the InP substrate. The interconnection between subcells is performed in a biaxial arrangement, connecting the 0.74 eV InGaAs top-cells and the 0.55 eV InGaAs bottom cells in independent strings. The biaxial connection implies that in each of the directions of the wafer plane (x, y), only one type of subcell is connected (top or bottom), thus creating two independent arrays with four output terminals: two for the top-cell array and another two for the bottom-cell array. This way, the output voltage can be tuned by simply choosing an appropriate number of series-connected subcells in both arrays in order to externally connect them in parallel, which require the same voltage per branch. This interconnection scheme by Wanlass was patented in [147].

In the configuration proposed by Wherer [133], the fabrication of the epitaxial structure requires that the lattice-matched 0.74 eV subcell is grown prior to the buffer layers meant to adapt the lattice constant of the crystal to the 0.55 eV subcell. Such scheme requires the use of an inverted structure, i.e. the radiation first impinges the InP SI substrate. This is possible because the SI-InP is transparent to the wavelengths of interest in this case. Each cell consists of a tandem DH
adopted from the previous work by Wanlass [120]. Another particularity of this design is that an electrical insulator is required in order to interconnect top-cell and bottom-cell independently. For that purpose, in [133] the authors employ a CID adopted from the previous design by Wanlass [120, 132]. The electrical isolation strategy based on a CID consists of an n/p/n structure (where the p-region is lightly doped and the n-regions have a relatively high doping) that prevents current leakages from the subcells of a tandem structure. Furthermore, the n-region from the CID is used to make subcell series-connection (of the same type) within the MIM, since they are in electrical contact with the LCL (also of n-type) of each subcell from the tandem.

The results presented in [133] show a FF of 50% (and a \(J_{SC}=0.7\) A/cm\(^2\)) for the 0.55 eV cells and a FF of 65% (and a \(J_{SC}=0.2\) A/cm\(^2\)) for the 0.75 eV cells. The isolation provided by the diode was sufficiently good, achieving leakage currents below 2 mA/cm\(^2\) for voltages comprised between -6.2 V and 8.8 V.

In the following work published by Wherer [148], substantial changes were made with respect to the first design. In the original design, each subcell had to be electrically isolated from each other within each tandem device (by means of CID) in order to connect them externally according to the aforementioned biaxial scheme. However, under this new strategy, both top and bottom subcells are series-connected through a tunnel junction, as it is the case in conventional MJSC. The latter requires that both subcells generate the same photocurrent for a given spectrum, which adds an important constraint to the cell design. Regarding TPV applications, an optimal bandgap configuration for a blackbody radiation spectrum at 1000 °C was identified as 0.74 eV/0.63 eV. The 0.74 eV value is imposed by the fact that a particular stoichiometry for the In\(_{x}\)Ga\(_{1-x}\)As alloy has to be chosen so that it lattice-matches the InP substrate used for the epitaxial growth of the stack. This tunnel junction strategy replaces the biaxial interconnection scheme by the series-connection of tandem cells. To do this, the “classic” scheme proposed by Wilt [108] is employed, in which the isolation channels penetrate to the SI-InP substrate. Furthermore, Wherer keeps the inverted configuration proposed in his initial design, which reports an important advantage that consists of the possibility of metalizing completely the rear side of the device, thus avoiding lateral conduction of current within the semiconductor. This configuration (Figure 3-g) allows the use of an n/p scheme (for each subcell) with just one n-type LCL on top of the n-type emitter of the top-cell, which eliminates the need of extra tunnel junctions typically needed for npn-MIM configuration presented in [80] (Figure 3-d).

The new results obtained by Wherer were significantly better than the ones obtained with his original design for independent tandem cells: \(V_{OC}=0.69\) V and \(FF=70.2\)% (for \(J_{SC}=0.284\) A/cm\(^2\)) and for the whole MIM consisting of 10 series-connected tandem cells: \(V_{OC}=6.14\) V and
FF=67.6% (for $J_{SC}=0.282\ \text{A/cm}^2$). The $V_{OC}$ of the whole MIM was lower than expected, which was attributed to isolation problems between MIM subcells.

During 2004, several works were published based on Wherer’s concept and focused on different technological features: Wherer himself [149] kept working on the fabrication and optimization of the 0.52 eV InGaAs subcell of the MIM. Wilt [150] studied the influence of including buffer layers within the tandem structure in the performance of the 0.74 eV top subcell. Wanlass and Albin [151] presented a theoretical circuit analysis of double-junction MIM devices.

It is worth mentioning the work by Siergiej [152], also from Bechtel Bettis, who developed, in collaboration with Essential Research a tandem cell on InP based on a new architecture, which included a 0.72 eV InGaAsP junction replacing the 0.74 eV InGaAs one proposed by Wherer. However, no MIM devices were fabricated using this tandem architecture. InGaAsP is lattice-matched to the 0.6 eV InGaAs bottom subcell and lattice-mismatched to the InP substrate. Thus, under this new scheme, the layer ordering during the growth is altered (Figure 3-h): first the buffer layers are grown to make the transition of lattice constants from InP to the InGaAs ternary corresponding to 0.6 eV, then, the 0.6 eV InGaAs is grown and finally, the 0.72 eV InGaAsP lattice-matched junction is grown. With this scheme no buffer layers are included between top and bottom junctions. Moreover, this way, an inverted configuration is not required as the light directly impinges the epitaxial side. Avoiding inverted design implies, on the one hand, that the use of a front grid metallization is again necessary and on the other hand that a tunnel junction is required to electrically connect the p-type base of the bottom junction and the n-type LCL. In this regard, this design takes advantage of the relatively high conductivity of the InPAs buffer layers to use them as LCL. In [152] the electrical and optical properties of these devices are analyzed in order to optimize their structure.

In 2006, Newman [153] from Emcore Photovoltaics, in collaboration with Bechtel Bettis Inc. kept developing the scheme by Siergiej [152] (Figure 3-h). He proposed the use of 0.72 eV AlGaInAs, instead of InGaAsP, to fabricate the top junction. They fabricated three versions of 2x2 cm$^2$ single-junction MIM composed of 30 cells: one with 0.6 eV InGaAs cells, another one using 0.72 eV InGaAsP and a third one employing 0.72 eV AlGaInAs. Very satisfactory $V_{OC}$ values were obtained, including 14.4 V for the two 0.72 eV cells. All FF values were higher than 70%. After that, they incorporated these structures in a double-junction MIM with 30 series-connected tandem cells. The InGaAsP/InGaAs MIM produced a $V_{OC}$ of 24.9 V with a FF of 68.5% (for $J_{SC}=30.75\ \text{mA/cm}^2$) and in the case of AlGaInAs/InGaAs, the $V_{OC}$ achieved surpassed 25 V and the FF was 67.6% (for $J_{SC}=32.25\ \text{mA/cm}^2$).

$^5$ Actually, the design presented by Siergiej does not impose any constraint regarding the use of an upright or inverted configuration, since the layer ordering of the epitaxial growth could be easily modified so that the 0.6 eV InGaAs junction is grown at the end, enabling the use of an inverted structure.
b) MJSC-MIMs for CPV

Previous works devoted to MIM for CPV applications agreed in the necessity of moving to MJSC to further boost the efficiency. The optimum bandgap distribution of double-junction cells adapted to AM1.5D spectrum is close to the 1.4 eV and 1.85 eV. These bandgaps are typically obtained using Ga(In)As and GaInP materials lattice-matched to GaAs. In 2010, Helmers, from Fraunhofer ISE, designed and developed a first prototype of dual-junction MIM for its use in large concentrator dish reflector systems working at 1,000 suns [154, 155]. The works deal with the design and performance of two MJSC epitaxial growth strategies: the lattice-matched approach and the metamorphic one with Ga$_{0.35}$In$_{0.65}$P and Ga$_{0.83}$In$_{0.17}$As subcell materials, both on SI GaAs. The MJSC MIM follows the integrated bypass diode design by Loeckenhoff [88] (Figure 2-f), except for incorporating a dual-junction device instead of a single-gap cell. Under this approach, two LCL are used together with three different tunnel junctions. In this case, the LCLI includes a graded buffer that accommodates the lattice constant, absorbing most of the strain generated during the growth of the metamorphic layers.

Different processing techniques applied to semiconductor layer etching and device isolation were implemented and compared with the aim of reducing the interconnection trench width, which in turn minimizes shading losses. An orthophosphoric-based wet chemical etching with the advantages of stopping at designated layers, high throughput and cheap processing was compared to a non-selective reactive ion etching in inductively coupled plasma (RIE-ICP), which obtained accurate structuring, undercut suppression and definable profiles. Device isolation techniques were also compared: on the one hand, a photo-definable polyimide, allowing easy structuring process and improvement of long-term device stabilization, and on the other hand SiN$_x$ layers applied by plasma-enhanced chemical vapor deposition (PECVD) even at vertical sidewalls and beneath overhangs, which combined with RIE-ICP, allows higher structuring accuracy and hence, an effective reduction of the safety margins. All this progress in advanced processing techniques adapted to MIM manufacturing enabled a reduction of the nominal interconnection trench widths from 100 μm to 53 μm, which in turn reduced shading losses from 10.9% to 5.7% and obtained an efficiency of 26% at an illumination of 500 suns.

In recent times, K. Watanabe, from the University of Tokyo, presented a MIM concept similar to the one reported in [97] except for the use of GaInP/GaAs dual-junction solar cells instead of the previous GaAs single-junction structures. A poor performance was achieved by the 10-cell MIM, which was attributed to the existence of current leakage paths between subcells [156].
8. Compilation and future developments

During the last few decades, the MIM technology has reached a high level of development, demonstrating in some cases the feasibility of industrial manufacturing. Table 1 compiles electrical data together with the corresponding information on MIM structure, isolation strategy, substrate used, application, type of illumination regarding most relevant MIM structures that have been fabricated and published to date. Only actual experimental results and not the product from modeling or theoretical projection are shown in the table.

Despite the high level of development of MIM technology, there are still a number of PV cell designs that have not been implemented in MIM arrangements (e.g. triple or quadruple junction solar cells) or that have not led to satisfactory results yet (Sb-based MIMs). These kinds of devices are not readily fabricated into a MIM configuration, mainly because their active semiconductor layers are not easily grown on the existing SI substrates (GaAs and InP). Thus, the fabrication of MIMs out of these cell structures require further technological developments, either concerning the use of SOI substrates, ELO techniques or processes that enable the combination of semiconductors with dissimilar lattice constants, such as the use of buffer layers or wafer bonding techniques.

Of particular relevance is the case of triple junction solar cells, which are mandatory to make MIMs competitive in CPV applications. Some possible future triple-junction MIM designs may keep the current state-of-the-art Ge/InGaAs/GaInP cell structure by using Ge-on-Insulator (GOI) substrates [157] or growing epitaxial Ge layers on SI GaAs substrates [158]. Other options are based on SI-GaAs and require the growth of ~1eV materials, such as GaInNAsSb [159] or strain balanced (SB) quantum nanostructures [160]. Among the options concerning the use of SI substrates, it is also worth mentioning the possibility of fabricating MJSC on SI-InP substrates [161]. Apart of the aforementioned alternatives based on SI substrates, it is also eventually possible to use the inverted metamorphic (IMM) configuration [162] combined with ELO techniques to transfer the active epitaxial layers of a GaAs-based multijunction cell to an insulating substrate. Also, the wafer bonding of GaAs and InP may lead to the fabrication of multijunction cell structures on SI substrates [163].

Another field susceptible of future developments is the Sb-based MIMs, which have not led to satisfactory results yet. These devices are needed in the field of TPV, especially for low temperature applications requiring very low bandgaps (below 0.6 eV) that are not readily manufactured with InGaAs-on-InP compounds. Recent works on the direct growth of GaSb layers on GaAs include the use of buffer layers [139-141] and interfacial misfit (IMF) arrays [164-167]. Other future developments might be based on different kinds of wafer bonding and ELO techniques [168, 169].
Finally, it is worth mentioning the recent advancements on the many possibilities of manufacturing of III/V semiconductors on Si substrates [170-172]. The progress in this field may enable the drastic reduction of the cost of PV devices based on III-V compounds, including MIMs.

9. Conclusions

In this article we have presented a thorough review of the historical development of MIM technology for HIPV applications, establishing the current state of the art and identifying the pending developments for the future. The different kinds of MIM designs that have been developed so far are described, summarized, and classified attending to the semiconductor material, the electrical isolation technique, and the final application. This provides a comprehensive overview of the most promising technological options, along with an insight in the possible future developments.

The review includes a brief description of the Si-based MIMs, which were mainly investigated during the early stages of MIM development, and focused mostly on solar applications. Since the early 80’s most of the research activities moved to III-V semiconductors, either for CPV, LPC and TPV applications. Most remarkable results in this field have been obtained for devices grown on SI GaAs and InP substrates. The former leading to the fabrication of GaAs LPCs and (In)GaAs/GaInP dual junction solar cells; the latter enabling the fabrication of 0.55-0.74 eV InGaAs based MIMs, both single and dual junction and devoted to LPC as well as to TPV applications. Other devices based on the (more challenging) CID and SOI isolation approaches have been also developed, demonstrating good performance.

All these achievements have moved MIM technology to a high level of development, in some cases even demonstrating the feasibility of industrial production. Future developments are still needed to implement some kinds of PV cell structures on MIM arrangements, especially concerning triple and quadruple junction MIMs and high efficient Sb-based TPV cells. The future MIM development may eventually take advantage of the recent advancements in the fields of buffer layers growth, ELO techniques and novel SOI substrates that may enable the fabrication of MIMs on a greater number of semiconductor compounds.

10. Acknowledgements

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<table>
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<th>Substrate</th>
<th>Application</th>
<th>Illumination</th>
<th>Electrical characteristics</th>
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<tr>
<td>1J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>30 subcells</td>
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<tr>
<td>1J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>30 subcells (4 cm&lt;sup&gt;2&lt;/sup&gt;)</td>
<td>Isolation grooves on</td>
<td>InP wafer</td>
<td>TPV</td>
<td>950 °C graybody-like halogen-lamp Radiation (average data from large sample): J&lt;sub&gt;SC&lt;/sub&gt;=2.23 A/cm&lt;sup&gt;2&lt;/sup&gt; (±0.002 A/cm&lt;sup&gt;2&lt;/sup&gt;); V&lt;sub&gt;OC&lt;/sub&gt;=10.93 V (±0.023 V); FF=68.3% (±0.783%); Eff.=20.3% (±0.27%) [129]</td>
<td></td>
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<tr>
<td>1J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>15 subcells</td>
<td>AlGaAsSb CID</td>
<td>Conductive p-GeSb wafer</td>
<td>TPV</td>
<td>Flash illumination</td>
<td>V&lt;sub&gt;breakdown&lt;/sub&gt; ~ 5-7 V V&lt;sub&gt;OC&lt;/sub&gt;(1 subcell)= 0.2 V; V&lt;sub&gt;OC&lt;/sub&gt;(MIM)= 0.42 V [134]</td>
<td></td>
</tr>
<tr>
<td>1J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>2 and 10 subcells (0.05x0.5 cm&lt;sup&gt;2&lt;/sup&gt; per subcell)</td>
<td>Isolation grooves on</td>
<td>GaAs wafer</td>
<td>TPV</td>
<td>-</td>
<td>2 subcell: J&lt;sub&gt;SC&lt;/sub&gt;=0.4 A/cm&lt;sup&gt;2&lt;/sup&gt; leads to V&lt;sub&gt;OC&lt;/sub&gt;=0.37 V and J&lt;sub&gt;SC&lt;/sub&gt;=1 A/cm&lt;sup&gt;2&lt;/sup&gt; leads to V&lt;sub&gt;OC&lt;/sub&gt;=0.47 V 10 subcell MIM: J&lt;sub&gt;SC&lt;/sub&gt;=0.4 A/cm&lt;sup&gt;2&lt;/sup&gt; leads to V&lt;sub&gt;OC&lt;/sub&gt;=1.8 V and J&lt;sub&gt;SC&lt;/sub&gt;=1 A/cm&lt;sup&gt;2&lt;/sup&gt; leads to V&lt;sub&gt;OC&lt;/sub&gt;=2 V [135]</td>
<td></td>
</tr>
<tr>
<td>1J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>10 subcells (5x5 nm&lt;sup&gt;2&lt;/sup&gt; per subcell)</td>
<td>Isolation grooves on</td>
<td>GaAs wafer</td>
<td>TPV</td>
<td>1,000 °C graybody source (emissivity~0.3)</td>
<td>J&lt;sub&gt;SC&lt;/sub&gt;=292 mA/cm&lt;sup&gt;2&lt;/sup&gt;; V&lt;sub&gt;OC&lt;/sub&gt;=6.14 V; FF=67.6% [150]</td>
<td></td>
</tr>
<tr>
<td>2J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>30 subcells (2x2 cm&lt;sup&gt;2&lt;/sup&gt;)</td>
<td>Isolation mesas on</td>
<td>InP wafer</td>
<td>TPV</td>
<td>1.925 °C equivalent blackbody temperature</td>
<td>InGaAsP/InGaAs: J&lt;sub&gt;SC&lt;/sub&gt;=30.75 mA/cm&lt;sup&gt;2&lt;/sup&gt;; V&lt;sub&gt;OC&lt;/sub&gt;=24.9 V; FF=68.5% AlGaInAs/InGaAs: J&lt;sub&gt;SC&lt;/sub&gt;=32.25 mA/cm&lt;sup&gt;2&lt;/sup&gt;; V&lt;sub&gt;OC&lt;/sub&gt;=25.1 V; FF=67.6% [153]</td>
<td></td>
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<tr>
<td>2J-GeAs polyimide/SiO&lt;sub&gt;2&lt;/sub&gt; /TiAu/SiO&lt;sub&gt;2&lt;/sub&gt;</td>
<td>No data for the number of</td>
<td>Isolation trenches</td>
<td>GaAs wafer</td>
<td>CPV</td>
<td>496 suns /1,028 suns</td>
<td>496 suns: V&lt;sub&gt;OC&lt;/sub&gt;=53 V; FF~80%; Eff.=26% [154]</td>
<td></td>
</tr>
<tr>
<td>MJSC (2 LCL, 3 tunnel junctions and bypass diodes)</td>
<td>subcells (4.368 cm²)</td>
<td>on SI GaAs</td>
<td>(AM1.5d)</td>
<td>1.028 suns: $V_{OC} \approx 54.5$ V; $FF \approx 73%$; Eff. $\approx 24.2%$</td>
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Figure 1. Operation principle of a typical MIM based on SI-substrate
Figure 2. Si-based MIM designs.
Figure 3. III-V semiconductor based MIM designs.
Figure 4. Typical III-V-based MIM layouts for LPC, CPV and TPV applications. (a) LPC MIM layout (reproduced from [82]), (b)-(c) conventional LCL-based MIM layouts for CPV and TPV applications (reproduced from [123]) (d) Interdigitated MIM design (reproduced from [123]).
Figure 5. GaAs MIM devices fabricated by means of CLEFT process, according to reference [67].
Figure 6. (a) 10x10 cm² compact concentrator module (CCM) comprising GaAs MIMs (reproduced from [89]). (b) 10x10 cm² TPV module comprising 0.6 eV InGaAs MIMs and filter for RTPV applications (reproduced from [131]).