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Design of Inductive Power Transfer System with a Behavior of Voltage Source in Open-Loop Considering Wide Mutual Inductance Variation

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Abstract—This work analyses the fundamental aspects of the principal resonant network topologies for Inductive Power Transfer Systems. From the analytical derivation, different features such as the variation of the coupling factor or load changes are studied. Based on this study, a resonant series-series topology is selected, which cancels the leakage inductance.

Due to the specifications, the air gap distance and its tolerance ($\pm 12\%$) produce a wide variation of the coupling factor of the inductive link. Therefore, through fast 3D simulations by finite elements, the inductive link is designed so that this variation is minimized. This maintains the resonance and the designed system, in turn, behaves like a voltage source regardless of power, and thus evades the implementation of a control stage.

A dc-dc converter is constructed where the distance between the primary and secondary side vary from 35 mm to 45 mm, and the power ranges from 0 kW to 5 kW. The output voltage range goes from 38 V_{DC} to 55 V_{DC} . Experimental results for the dc-dc converter report an efficiency of over 94%.

Index Terms—Inductive link, Inductive Power Transfer system (IPT system), Resonant Converter, Resonant Network, Wireless Power Transfer system (WPT system).

I. INTRODUCTION

INDUCTIVE power transfer (IPT) systems is a continuously growing industry [1] thanks to its many benefits compared to electrical connection in some applications, e.g. the user does not need to interfere in the connection. Electric vehicles [2]–[7], battery charging for portable consumer electronics [8]–[10] and implantable devices [11]–[14] are an important focus of the IPT systems. In some of the before mentioned industrial applications, cost, maintenance or even mechanical limitations make the use of physical connection a difficult task. Therefore, IPT systems represent a very important opportunity to solve these inconveniences.

The common configuration of an IPT system can be shown in Fig. 1. The air-gap between the primary and the secondary inductor and its geometries will determine the self-inductance and the mutual inductance of the inductive link. Constant air-gap IPT converters and its control designs are widely studied in the literature [15]–[17]. IPT systems with variable air-gaps

where the mutual inductance varies but self-inductances are practically constant are also well-known [2], [3], [10], [18]. Nevertheless, some applications have a wide air-gap variation between primary and secondary side which can change not only the mutual inductance but also the self-inductance. In this way, the tuning between transmitter/receiver and its resonant network may be misadjusted. If this happens, the resonant frequency and the operating point of the system will vary.

It can be solved using a frequency control that is extensively studied in the literature [2], [16]–[23].

For better understanding of the system design, this paper examines three different resonant compensation topologies: a) Typical Series-Series resonant compensation where the capacitors of the resonant network are tuned to the self-inductance of the coil [3], [4], [7], [8], [10]; b) Common Series-Parallel resonant compensation where the capacitors are tuned to part of the primary self-inductance of the coil and fully tuned to the secondary coil [5]–[7], [11], [13], [14]; c) Series-Series resonant compensation where the capacitor cancels the leakage-inductance of both primary and secondary coils [24].

From these analyses, the three topologies are studied and the criterion for the selection of the resonant network that adapts to the specifications of the system is defined: power, transformation ratio of the voltage and current, the behavior of the system, the air-gap variation, etc.

Based on this analysis, the selected topology is Series-Series leakage resonant compensation. The tuning method of this topology depends on the leakage-inductance. It is this that depends on the coupling factor and the self-inductance.

This paper focuses on the design of inductive links for series-series leakage resonant compensation to achieve constant leakage-inductance over the entire range of air-gap distances while maintaining high efficiency based on previous studies [25], [26]. Fast eddy current 3D Finite Element simulations are carried out using homogeneous models presented in previous work [25], [27]–[29] to allow simulations of inductive links with Litz-wire windings to be performed.

In this paper, for the sake of clarity, the leakage inductance and the quality factor of an uncomplicated coil are obtained by conducting a set of finite element parametric simulations. Later, electrical simulations of the inductive link are performed to evaluate whether the chosen topology can operate as a

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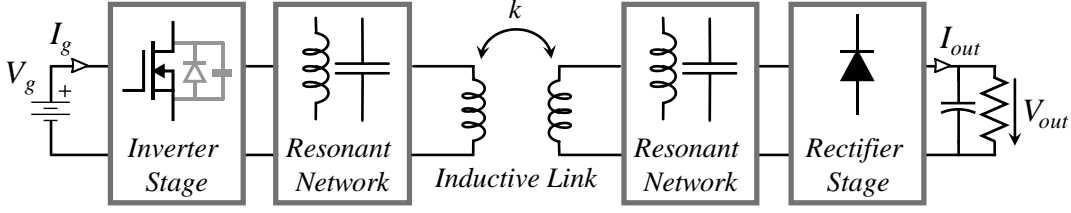


Figure 1: Illustration of an inductive power transfer system: inverter stage, typically a full-bridge, primary resonant network based on capacitors and inductors, inductive coils, secondary resonant network (which can be different to the primary network), and a rectifier stage, typically diodes because it simplifies the control.

constant voltage source, independent of load and air space variation.

Also, the design methodology presented in this study is validated using an IPT system designed for a large power range that goes from 0 kW (no load condition) up to 5 kW. The input voltage of the system will be $400 V_{DC}$, the operating output voltage range goes from $38 V_{DC}$ to $55 V_{DC}$.

This paper is divided into five sections. Section II studies the fundamental analysis of an IPT system by deriving a simple electrical analysis. First, the first-harmonic approximation is used to simplify the analysis by modeling the inverter and the rectifier with an AC voltage source and equivalent resistor respectively. Then, the inductive link is simplified by taking into account the resonant tank to simplify the analysis: a) by using a T-model for Series-Series resonant network. b) by using a cantilever-model for Series-Parallel resonant network. Based on this approximation, conventional series-series resonant topology, basic series-parallel resonant topology and series-series resonant topology where the capacitors are tuned with the leakage inductance are analyzed and described. In Section III seeks to outline the problem statement dealt with in this paper. Based on the specification of the system, one resonant compensation topology is selected. In Section IV, based on the previous fundamental analysis, a design criterion is found for the inductive links used in Series-Series leakage resonant compensation to mitigate the effects of the air-gap variation which will maintain the resonance of the system, and thus allow the system to operate as a voltage source in open-loop. In Section V, the built prototype is described and measurements and simulations are compared to validate the design criteria.

II. FUNDAMENTAL ANALYSIS OF IPT SYSTEM

Fig. 1 shows the typical architecture utilized in inductive power transmission. The inverter stage is habitually a full-bridge inverter, and the rectifier stage can be either a controlled full-bridge or a diode rectifier. In this work, the inverter stage is a full-bridge inverter, and the rectifier is a full-bridge diode rectifier.

The circuit is analyzed assuming the first harmonic approximation [15], [30]. It is reasonable to replace the inverter stage with an AC voltage source, see Fig. 2a. The rectifier is substituted with its equivalent resistor, as shown in Fig. 2b [30]. The behavior of the circuit, as a current source or voltage source, determines the value of the equivalent resistor.

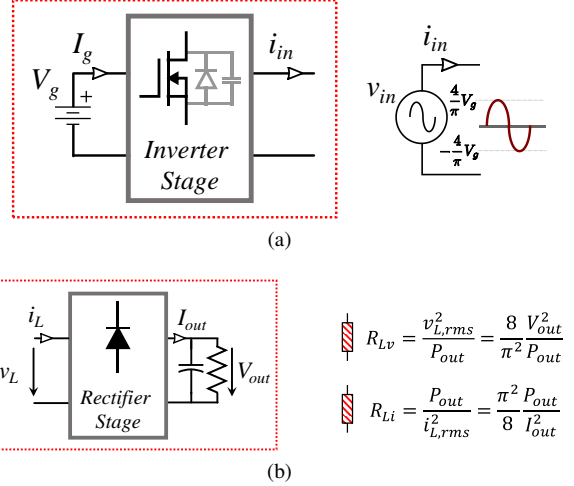


Figure 2: a) Equivalent AC input voltage source as presented by full-bridge mosfet inverter stage. b) Equivalent AC resistor as presented by full-bridge diode rectifier and load.

The inductive link showed in Fig. 1 can be modeled in different forms [31]–[34]: a) T-model as in Fig. 3a, b) Cantilever model as in Fig. 3b. Where L_p and L_s are the self-inductances of the primary and secondary side respectively, k is the coupling factor, R_p and R_s are the self-resistances of the primary and secondary coil respectively, and n is the ratio between primary and secondary side of the ideal transformer of each model.

For the sake of facilitating the analysis of the diverse topologies, we utilize the representation in T-model to analysis the series-series circuits, and the representation in the cantilever form is employed to study the series-parallel circuit.

In T-model, n is:

$$n_T = \sqrt{\frac{L_p}{L_s}} \cong \frac{N_p}{N_s} \quad (1)$$

And the relation n in the cantilever model is:

$$n_C = k \sqrt{\frac{L_p}{L_s}} \quad (2)$$

A. Series-Series Resonant Compensation

This topology is based on series-series resonant network that consists of a resonant capacitor placed in series with the

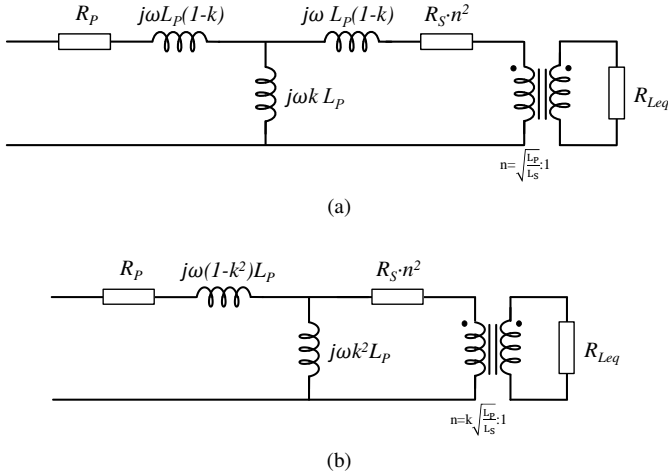


Figure 3: Representation of the equivalent circuit model of an inductive link: a) T-model representation. b) Cantilever model representation.

primary and the secondary inductor. Both resonant capacitors are tuned with the self-impedance of the inductive coils. The value of the capacitors at the switching angular frequency ω_s are:

$$C_p = \frac{1}{\omega_s^2 L_p} \quad (3)$$

$$C_s = \frac{1}{\omega_s^2 L_s} \quad (4)$$

Thus, the self-impedances L_p and $n^2 L_s$ with C_p and C_s/n^2 respectively behave as a short circuit at the resonance frequency as can be shown in Fig. 4a. The result of this is a T circuit that has the gyrator's behavior [35] so that, essentially, the output current of the quadripole depends on the gyrator's forward gain G and the input voltage. The input current and output voltage have an analog relation. That is:

$$\begin{aligned} I_s &= G \times V_p \\ I_{in} &= -G \times V_s \end{aligned} \quad (5)$$

Where G is:

$$G = \frac{1}{j\omega k L_p} \quad (6)$$

Regarding Fig. 4b, an abbreviated expression of the output current is readily derived using the gain of the gyrator as long as we depreciate the resistances (R_p and R_s) due to the non-ideal inductors. By the application of the gyrator equation, it follows that the output current depends on the frequency, the coupling factor, and the inductance value:

$$I_L = \frac{jV_{in}}{\omega n_T k L_s} \quad (7)$$

Due to the fact that the output current depends on the input voltage, which is constant like the inductance value and the coupling factor, it is straightforward to deduce that the equation (7) describes the behavior of a current source.

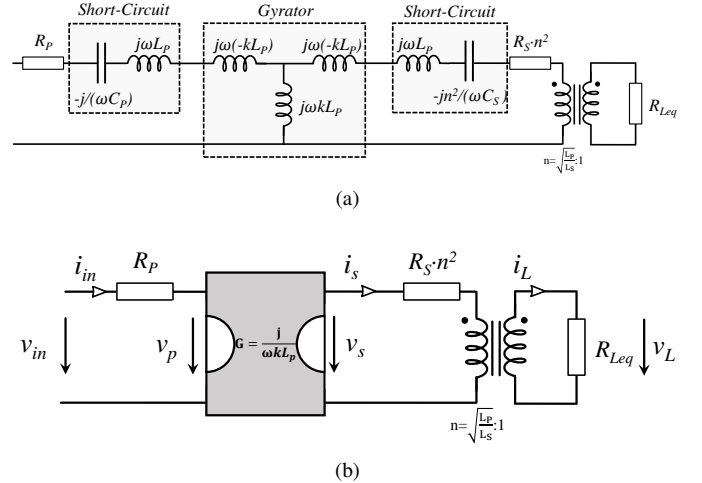


Figure 4: Schematic representation of: a) An IPT system with a series-series resonant network as current source. b) Equivalent circuit of a) when the resonance occurs.

Moreover, from the expression (7), it is immediately noticed that for a coupling factor k and an angular switching frequency ω_s , the value for the secondary inductance needed to achieve the output current expected is:

$$L_s = \frac{V_{in}}{\omega k n_T I_L} \quad (8)$$

Consequently:

$$L_p = \frac{n_T V_{in}}{\omega k I_L} \quad (9)$$

Therefore, it is possible to calculate the power losses due to the AC resistances of the coils as:

$$P_R = I_{in}^2 R_p + I_s^2 R_s n_T^2 = R_p (I_{in}^2 + I_s^2) \quad (10)$$

Using (5):

$$I_{in} = \frac{-jn_T V_L}{\omega k L_p} \quad (11)$$

Also, the secondary current is:

$$I_s = \frac{V_L}{n_T R_{Leq}} \quad (12)$$

Simplifying the expression (10) using (9), (11) and (12), the maximum efficiency of the link is:

$$\eta_{link} = 1 - \frac{2}{kQ} \quad (13)$$

B. Series-Parallel Resonant Compensation

This topology is based on a series-parallel resonant network that is composed of a resonant capacitor placed in series with the primary inductor and a resonant capacitor set in parallel with the secondary inductor.

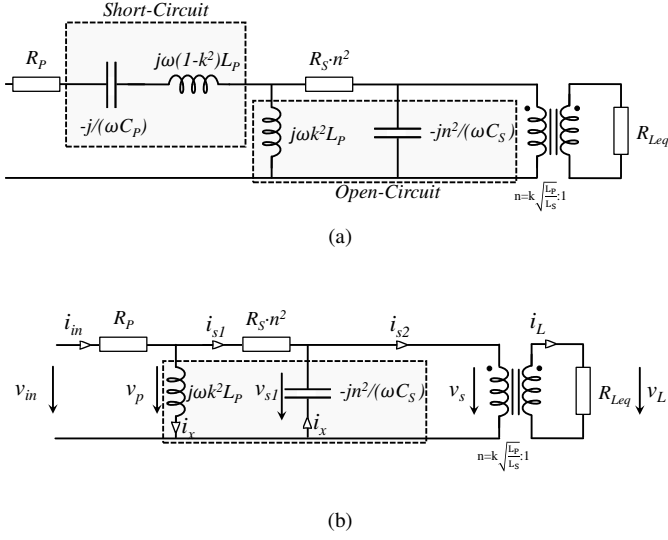


Figure 5: Schematic representation of: a) An IPT system with a series-parallel resonant network as voltage source. b) Equivalent circuit of a) when the resonance occurs.

The primary resonant capacitor is tuned with the primary self-inductance multiplied by a factor of $(1 - k^2)$; the secondary resonant capacitor is tuned with the secondary self-inductance. By doing so, it is possible to achieve a constant voltage source independently of the load.

Thus, it is necessary to define the value of the primary and secondary capacitor at the switching angular frequency ω_s as:

$$C_p = \frac{1}{\omega_s^2 L_p (1 - k^2)} \quad (14)$$

$$C_s = \frac{1}{\omega_s^2 L_s} \quad (15)$$

By doing so, the impedances $L_p(1 - k^2)$ on the left side and $L_p k^2$ on the right side with C_p and C_s/n^2 respectively behave as a short circuit and open circuit, reciprocally, at the resonance frequency, as can be shown in Fig. 5a.

In order to obtain the simplified expression of the output voltage, resistors due to the non-ideal inductors are neglected (see Fig. 5b). By the application of the Kirchhoff law, it follows that the output voltage depends on the input voltage, the inductive link turns-ratio and the nominal coupling factor:

$$V_L = \frac{V_{in}}{n_C} \quad (16)$$

From the equation (16), it is easy to verify that the behavior of this resonant topology is merely a voltage source because the output voltage only depends on the input voltage and the turn ratio of this topology.

Analyzing the power losses due to the AC resistances of the coils, it is possible to obtain an expression to calculate the optimal values of L_p and L_s in order to minimize the losses in the system. Thus, calculating the power losses as:

$$P_R = I_{in}^2 R_P + I_{S1}^2 R_s n_C^2 \quad (17)$$

Where I_{in} and I_{s1} are RMS values.

The secondary current I_{s2} can be calculated as:

$$I_{s2} = \frac{V_L}{R_{Leq} n_C} \quad (18)$$

Then, the input current is:

$$I_{in} = I_{s2} \quad (19)$$

The current through the parallel inductance can be expressed as:

$$I_x = \frac{V_{in}}{j\omega k^2 L_p} = \frac{V_L}{j\omega k^2 L_p n_C} \quad (20)$$

Thus, the current I_{s1} can be expressed by using (18) and (20) as:

$$I_{s1} = V_L \frac{\sqrt{\omega^2 k^4 L_p^2 + n_C^4 R_{Leq}^2}}{\omega k^2 L_p n_C R_{Leq}} \quad (21)$$

Where V_L is the RMS value.

Simplifying the expression (17) using (19) and (21) leads to:

$$P_R = \frac{P_{out}}{Q} \left[\frac{\omega L_s}{R_{Leq}} \left(1 + \frac{1}{k^2} \right) + \frac{R_{Leq}}{\omega L_s} \right] \quad (22)$$

Then, the calculation of the optimum value of L_s that minimizes the losses is:

$$\frac{dP_r}{dL_s} = 0 \rightarrow L_s = \frac{k R_{Leq}}{\omega_s \sqrt{k^2 + 1}} \quad (23)$$

Consequently using (16):

$$L_p = L_s \left(\frac{V_{in}}{k V_L} \right)^2 \quad (24)$$

Thus, using this expression in (22) the maximum efficiency of the link is:

$$\eta_{link} = 1 - \frac{2\sqrt{k^2 + 1}}{kQ} \quad (25)$$

C. Series-Series Leakage Resonant Compensation

The series-series resonant network is founded on a resonant capacitor in series with the primary inductor; also, a resonant capacitor is situated in series with the secondary inductor. In order to achieve in this configuration a constant voltage source, where the output voltage does not depend on the power demanded (load-independent), it is essential to tune the primary and secondary capacitors with the impedance that is in series with them, the leakage inductance.

Then, it is crucial to define the value of the primary and secondary capacitor at the switching angular frequency ω_s as:

$$C_p = \frac{1}{\omega_s^2 L_p (1 - k)} \quad (26)$$

$$C_s = \frac{1}{\omega_s^2 L_s (1 - k)} \quad (27)$$

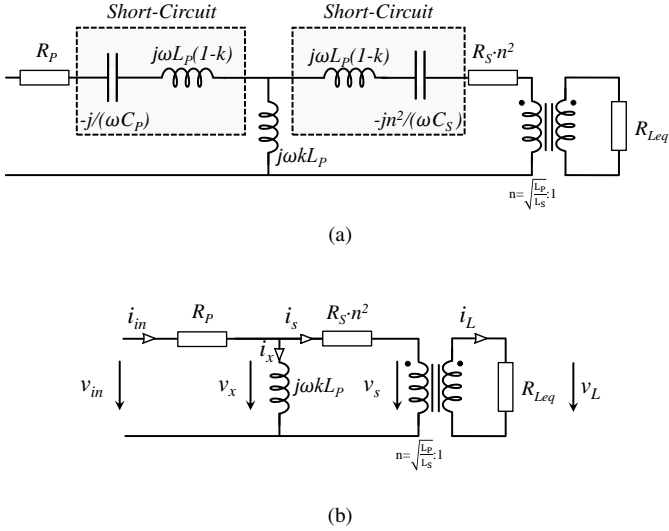


Figure 6: Schematic representation of: a) An IPT system with a series-series resonant network as voltage source. b) Equivalent circuit of a) when the resonance occurs.

By doing so, the impedances $L_p(1-k)$ and $L_s(1-k)$, with C_p and C_s respectively, work as a short circuit at the resonance frequency, as shown in Fig. 6a.

With the aim of obtaining the simplified expression of the output voltage, resistors due to the non-ideal inductors are neglected (see Fig. 6b). By the application of the Kirchhoff law, it follows that the output voltage depends on the input voltage (it is a voltage source as the previous topology) and the inductive link turns-ratio:

$$V_L = \frac{V_{in}}{nT} \quad (28)$$

Analyzing the power losses due to the AC resistances of the coils, it is possible to obtain an expression to calculate the optimal values of L_p and L_s in order to minimize the losses in the system. Thus, calculating the power losses as:

$$P_R = I_1^2 R_p + I_2^2 R_s n_T^2 \quad (29)$$

Where I_1 and I_2 are RMS values. The primary current can be expressed then as:

$$I_1 \cong V_{in} \frac{\sqrt{R_{Leq}^2 n_T^4 + \omega^2 k^2 L_p^2}}{\omega k L_p R_{Leq}} \quad (30)$$

And the secondary current as:

$$I_2 \cong \frac{V_{in}}{R_{Leq} n_T^2} \quad (31)$$

Where V_{in} is the RMS value.

Simplifying the expression (29) using (30) and (31) leads to:

$$P_R = V_L^2 \frac{\omega L_p}{Q} \left[\frac{R_{Leq}^2 n_T^4 + 2\omega^2 L_p^2}{R_{Leq}^2 n_T^2 \omega^2 L_p^2} \right] \quad (32)$$

Then, the calculation of the optimum value of L_s that minimizes the losses is:

$$\frac{dP_r}{dL_s} = 0 \rightarrow L_s = \frac{R_{Leq}}{\omega_s k \sqrt{2}} \quad (33)$$

Consequently:

$$L_p = L_s \left(\frac{V_{in}}{V_L} \right)^2 \quad (34)$$

Thus, using this expression in (29) the maximum efficiency of the link is:

$$\eta_{link} = 1 - \frac{2\sqrt{2}}{kQ} \quad (35)$$

III. PROBLEM STATEMENT AND SELECTION OF A COMPENSATION METHOD

The specification of the system is an IPT converter which operates with an input voltage of 400V and an output voltage range from 38V to 55V working from 0 kW up to 5 kW. The gap distance between the emitter and the receiver is $40\text{mm} \pm 5\text{mm}$.

Based on the previous mere mathematical derivations, the selection of resonant network compensation is explained in details from the standpoint of behavior (current or voltage source), efficiency, the air-gap variation, and the inductive link design (high current, voltage, and so on so forth).

In order to establish a basis for comparing the resonant network analyzed in this paper, a quality factor of 300 and a nominal coupling factor of $k = 0.3$ are considered.

The analysis of the resonant topologies starts with the comparison of the performances of the different inductive link according to the resonant network chosen. Series-Series resonant compensation presents the highest efficiency. Series-Parallel resonant compensation has 5% more losses than the first. The worst of these topologies is the Series-Series leakage-inductance resonant compensation, which has 40 % more. In terms of performance, and using the same values of Q and k, the results show that the conventional SS resonant compensation presents 97.77 % of efficiency, SP resonant compensation reports an efficiency of 97.67 %, and SS leakage-inductance shows 96.85 % of performance. Table I compiles the equation of the efficiency of each topology and the particular values for the considered coupling factor and quality factor.

The behavior of the SS resonant compensation, despite its superior efficiency, constitutes a challenge in ensuring that the output voltage in the required range. This is because this topology is a current source which means that the output current is constant and acts independently of the load. Since the output current is constant, the higher the load (the higher the power), the higher the output voltage must be. It is then possible to adjust the maximum voltage to meet the specifications. The problem occurs when the load tends to lower values, i.e., when the power is low (about 0 kW), the output voltage should decrease to 0 V. Then, the output voltage requirements are not met over the entire power range. Therefore, this topology is not adequate for this application.

The second most efficient topology is the one using SP resonance compensation. In contrast to SS resonant network, in SP, even if the load changes, the output voltage is constant.

Table I: Summary of the Resonant Topologies

Topology	Efficiency	Behavior	Ls Current
SS	$1 - \frac{2}{kQ} = 97.77\%$	Current Source	$111A_{rms}$
SP	$1 - \frac{2\sqrt{k^2+1}}{kQ} = 97.67\%$	Voltage Source	$430A_{rms}$
SS-Lk	$1 - \frac{2\sqrt{2}}{kQ} = 96.85\%$	Voltage Source	$111A_{rms}$

However, if the coupling factor changes due to the air-gap variations, the output voltage changes proportionally due to the turn-ratio of the system (it is defined by (2)). Moreover, this topology needs an output inductor connected in series between the tank capacitor and the power converter due to the parallel capacitor placed with the receiver.

Regarding coupling factor variations, and therefore output voltage fluctuations, due to the broad range of output voltage specified, it is possible to design the inductive link to reduce the variation of coupling factor, keeping the output voltage in range. By doing so, it is possible to eliminate the control loop and the wireless-communication latency problems. However, due to the fact that the control is eliminated, the ratio between the transmitter and receiver must be adapted to obtain a voltage ratio of 8.

The optimum value for the secondary inductor for this application should be 185 nH . Then if a voltage drop of $45 V_{rms}$ occurs in the receiver, the circulating current through the inductor is $430 A_{rms}$. This fact means that the amount of copper required to achieve a reasonable current density is considerable. For this reason, this topology is not very appropriate when the ratio between primary and secondary are large.

Even though SS leakage-inductance resonant compensation is the most critical from an efficiency point of view, it presents the most desirable characteristics from the behavior standpoint. Analogous to SP, this topology serves as a voltage source. Furthermore, the output voltage does not depend on the coupling factor if the resonance is kept. Additionally, the current through the receiver is the same that the output current of the system: $111 A_{rms}$.

In light of the above, the Series-Series leakage-inductance resonant compensation is chosen, where a design criterion for the inductive link is described and discussed in the following section.

For the purpose of summarizing, the Table

Table I summarizes the characteristics of each circuit described in this section.

IV. DESIGN CRITERION FOR SS LEAKAGE-INDUCTANCE RESONANCE TO OPERATE IN OPEN-LOOP

From C_p and C_s tuning method (eq. (26) and eq. (27)), it is trivial to deduce that the resonance depends on the self-inductance and the coupling factor, that is, on the leakage-inductance. Assuming that primary and secondary coils are identical, the inductive link at the nominal distance has a self-inductance L_0 and a coupling factor k_0 . Considering a variation in the distance, the self-inductance changes to L_1 and the coupling factor to k_1 . In this work, instead of using a control stage to change the frequency in order to keep the

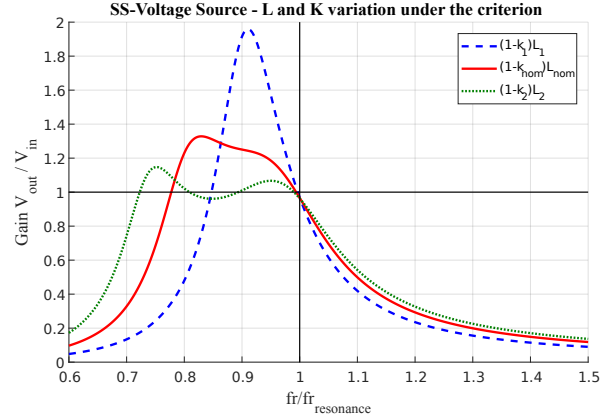


Figure 7: Simulated bode results of an inductive link that consists of a primary and secondary coil with a self-inductance of $115\mu\text{H}$ and a coupling factor of 0.25 when the self-inductance and the coupling factor change following the criterion (37).

output voltage in range, a design criterion to maintain the leakage inductance, and then the resonant condition in the inductive link even if the air gap varies is proposed. It is defined as:

$$(1 - k_0)L_0 = (1 - k_1)L_1 \quad (36)$$

In (36), it is easy to notice that the leakage-inductance must be constant over the whole range of air gap distances. By defining $L_1 = \alpha_L L_0$ and $k_1 = \alpha_k k_0$, the following expression is derived:

$$\alpha_k = \frac{\alpha_L + (k_0 - 1)}{k_0 \alpha_L} \quad (37)$$

Where α_L and α_k represents the variation of L_0 and k_0 respectively. If the variation of the self-inductance and coupling factor is irremediably in the required range of air gap, taking the criterion of (37), the same resonant frequency is kept, obtaining the voltage gain showed in Fig. 7. In this voltage gain, it is noticed that in the nominal frequency for each distance, there is unity gain.

However, this is an ideal case. In a practical case, if the design of the inductive link tries to follow the criterion, there are slight variations in the leakage inductance that modifies the output voltage value.

Fig. 8 shows the considered model for the inductive link which has two degrees of freedom: Xc and Z. The winding dimension is proportional to the core dimension in order to simplify the practical case.

In order to quantify these output voltage variations, a set of magnetostatic 3D simulations using a Finite Element tool, as Ansys Maxwell, in combination with the models presented in [25], [27]–[29] are performed. The parameter Xc is set to vary from 50mm to 300mm. Also, the air gap distance is set to vary from 20mm to 60 mm. From the design space mentioned above, the inductance and coupling factor (then the leakage inductance) of the simulated coil are obtained.

Fig. 9a shows the leakage inductance for each value of Xc and for the Z range from 30 mm to 50 mm. The black vertical

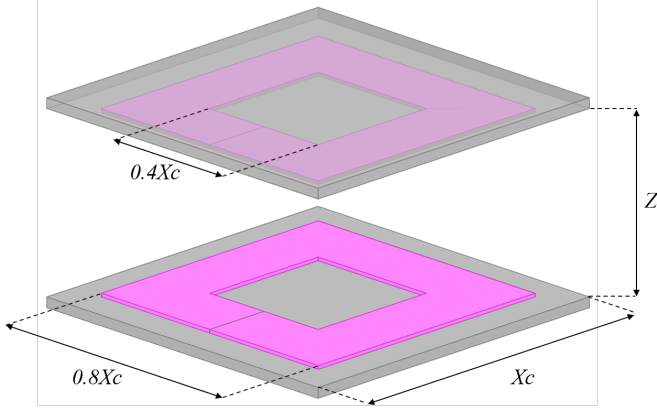


Figure 8: Geometry and parameters of the Finite Element model built in Ansys Maxwell ®.

lines limit the air-gap range analyzed. The horizontal black lines limit the maximum leakage inductance variation allowed for the nominal output voltage of $46.5 V_{DC}$ to be in range for this particular application.

Analyzing the leakage inductance normalized to the nominal case $Z = 40$ mm, the smaller the coil size is, the more constant the leakage inductance is in the analyzed Z range. Nevertheless, smaller coils in this air-gap distance also means smaller coupling factor and lower efficiency.

For this reason, in order to obtain the maximum performance complying with the limits presented in Fig. 9a, the giant inductive coil that fits in the available area of the specifications is selected. Then, from the air gap being $40\text{mm} \pm 5\text{mm}$ ($\pm 12\%$), the normalized leakage inductance to the nominal case is $[0.95, 1, 1.04]$.

Following the proposed design criteria, the voltage gain has smaller variations (see Fig. 9b). By keeping the nominal output voltage ($46.5 V_{DC}$), the output voltage at the maximum air-gap distance is $40 V_{DC}$, and the minimum air-gap is $50.2 V_{DC}$.

In this resonance mode, it can be seen that regardless of the power demand and the distance between coils, there is always a positive phase (see Fig. 9c). This fact guarantees that ZVS can be obtained, thus reducing the losses of the inverter stage.

V. EXPERIMENTAL RESULTS

A prototype is built to verify the theoretical analysis.

The inverter employed in this prototype is made of two evaluation boards designed by CREE (each evaluation board is a half-bridge module KIT8020CRD8FF1217P-1). The switches mounted on the evaluation boards modules are SiC MOSFETs from CREE model C2M0025120D. The diodes are C4D20120D since they only need to conduct during the dead-times of the inverter. The primary resonant capacitor is formed by 3 capacitors in parallel and 6 in series to get a final value of 34 nF using $2 \text{ kV } 68 \text{ nF B32654}$ capacitors from TDK.

The coils have been manufactured using ferroxcube 3C95 ferrite tiles. The size is $200 \text{ mm} \times 200 \text{ mm}$. The geometry is similar to that of E-Cores (see Fig. 10). The litz wire consists of 630 strands of 0.1 mm diameter following the design guidelines of [26]. The primary inductive link has 16

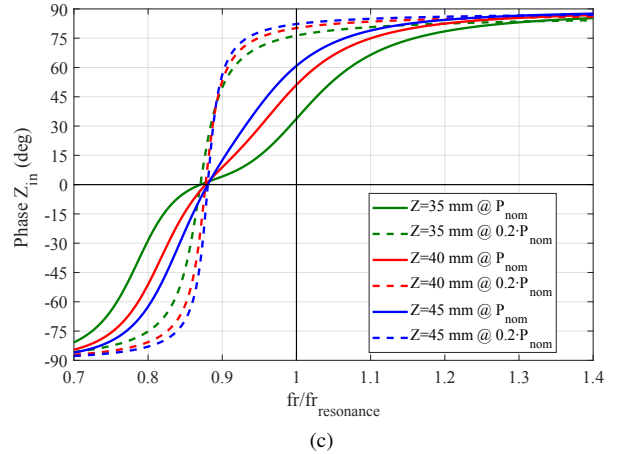
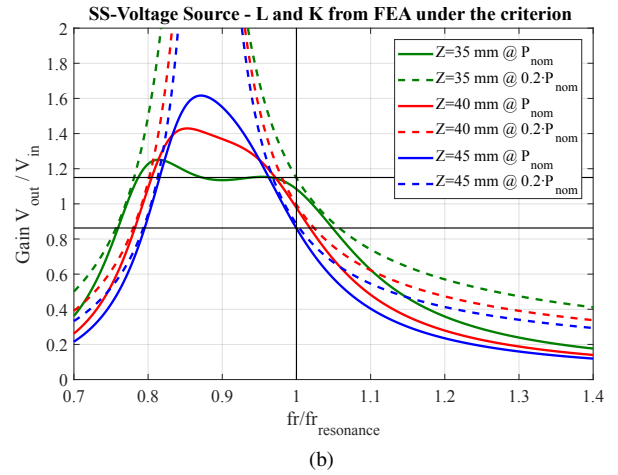
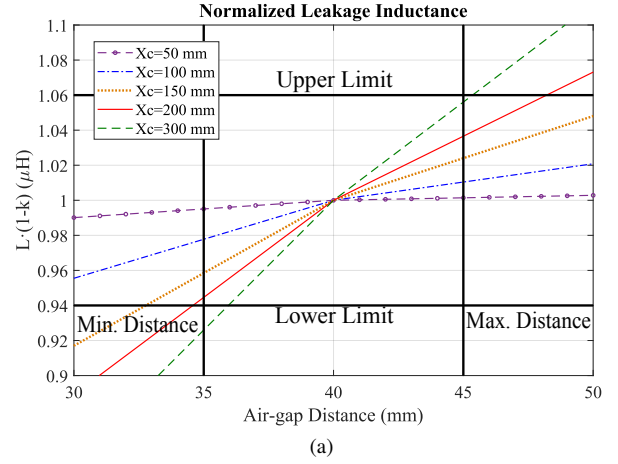


Figure 9: Results of the FEA model a) Leakage inductance finite element results for different X_c values. b) The Gain voltage of the system when the self-inductance and the coupling factor change trying following the criterion (37). c) The phase-angle of the input impedance of the system when the self-inductance and the coupling factor change trying following the criterion (37).

turns in series; the secondary has 2 turns in series and 8 in parallel due to the high current. Results of finite element

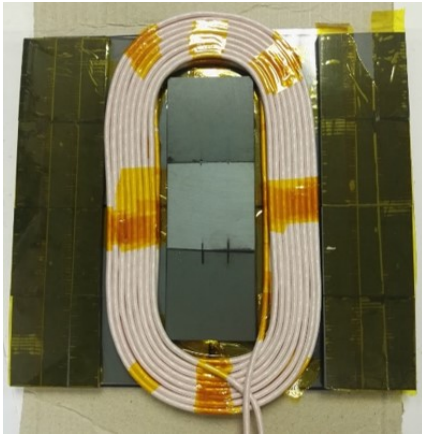


Figure 10: Photograph of the primary IPT coil with a winding made from litz wire with 630 strands of 0.1 mm diameter.

Table II: Inductive Link Specifications

Distance (mm)		L_p (μH)	L_s (μH)	k (%)	$\frac{L_{Lp}}{L_{Lpnom}}$	$\frac{L_{Ls}}{L_{Lsnom}}$
35	FEA	117	2	37		
	MEA	119	2.18	35	0.983	0.965
40	FEA	113	1.9	32		
	MEA	114	2.14	31	1	1
45	FEA	111	1.9	28		
	MEA	113	2.12	26	1.063	1.068

analysis (FEA) and measurement (MEA) for the primary and secondary inductances, as well as the coupling factor, are presented in Table II. FEA and MEA exhibit good matching in all the cases.

For the receiver, the resonant capacitor has 27 parallel capacitors 1.6 kV 68nF B32654 from TDK to obtain 1.8 μF of capacitance. The rectifier is made of 160 A 100 V STPS160H100TV diodes from STMicroelectronics. Finally, the output capacitance is made of 30 capacitors of 3.3 μF and 250 V from TDK, model B32654.

The output voltage and efficiency results along the whole power range are presented in Fig. 11. From 1 kW on, the output voltage at the two extreme gap distances are within the limits and with high efficiencies (more than 90 %). At no load and 45 mm of gap distance, the voltage goes beyond the limits. This is the reason why a dummy load of 50 W is added, leading to a maximum output voltage of 55 V.

Fig. 12 shows the capture of the oscilloscope at 5 kW and the lowest distance (35mm). The measures presented are the gate-source voltage and drain-source voltage of an inverter Mosfet, the primary current and the input current when the input voltage is 400 V. Due to the parasitics of the converter, we have adjusted the switching frequency to 110 kHz to reach the desired output voltage value along the z-range air gap. The current shows an inductive behavior, helping to achieve ZVS, as no overshoot in the drain-source voltage is presented.

Fig. 13 also shows the same waveforms at the 5 kW point, but at the highest distance (45mm). An interesting finding is the higher phase in current, going from 13.6 A in the previous case to 16.5A; although, it still presents inductive behavior. A particular point to be highlighted is since the air-gap distance

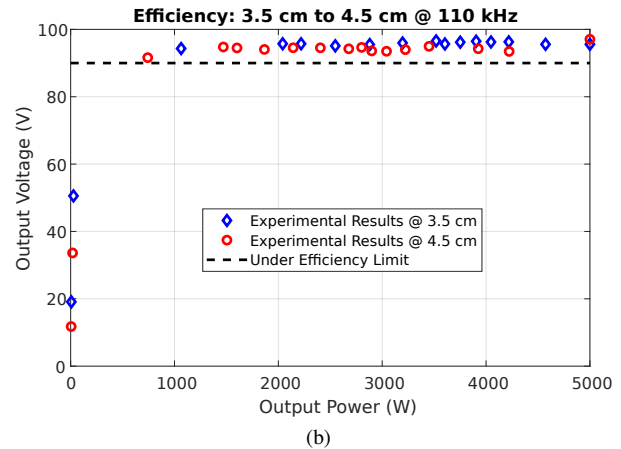
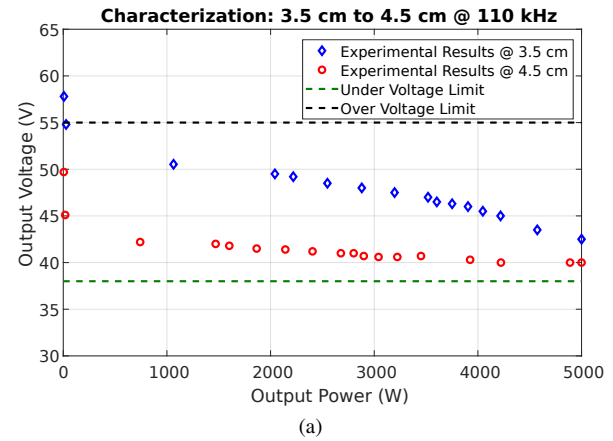


Figure 11: Measurement results of the dc-dc converter when the output power is varied: a) Output Voltage. b) Efficiency of the IPT system.

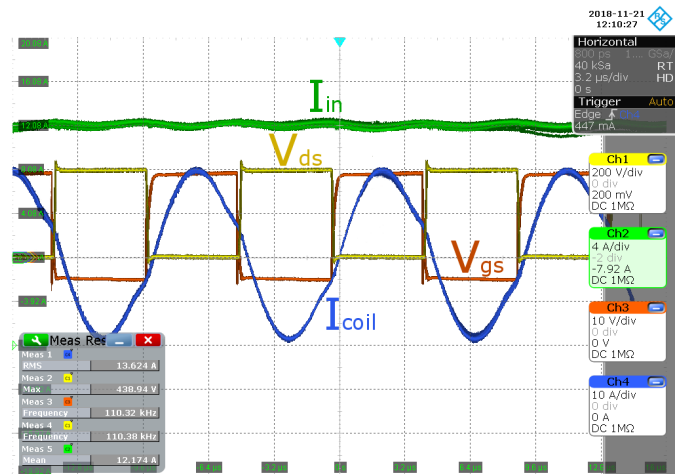


Figure 12: Measured waveforms of the dc-dc converter for an air-gap of 3.5 cm at nominal power. The green waveform is the input DC current of the system, the blue waveform is the resonant AC current of the primary coil, the yellow waveform is the drain-source of one low-side mosfet of the inverter, the orange waveform is the grate-source of the same mosfet.

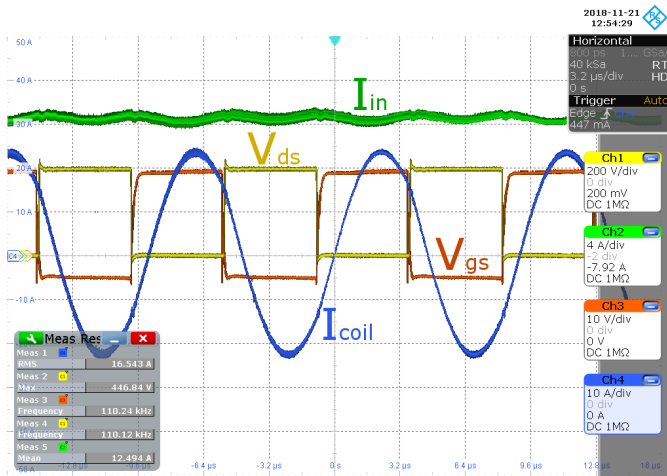


Figure 13: Measured waveforms of the dc-dc converter for an air-gap of 4.5 cm at nominal power. The green waveform is the input DC current of the system, the blue waveform is the resonant AC current of the primary coil, the yellow waveform is the drain-source of one low-side mosfet of the inverter, the orange waveform is the gate-source of the same mosfet.

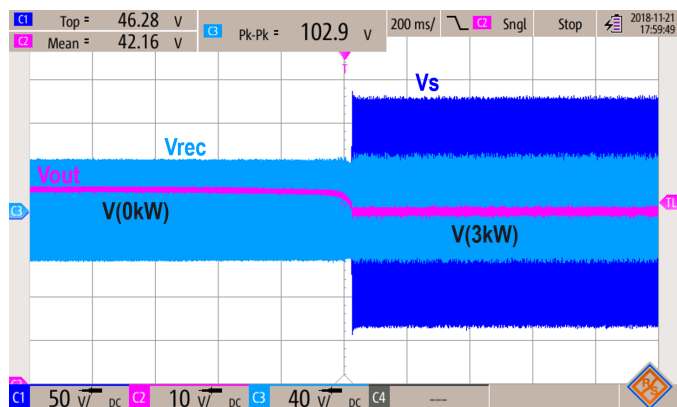


Figure 14: Waveforms measured during power step from 0kW to 3kW. The pink waveform is the output voltage, the light blue waveform is the rectifier voltage, the dark blue waveform is the voltage of the secondary coil.

increases, the magnetizing inductance decreases (see Fig. 6b). Due to that fact, the system consumes a higher reactive power, in turn, increasing the phase angle of the input impedance. For this reason, the current peak in the farthest position is higher.

Fig. 14 shows a power step from 0 kW to 3 kW, where it is possible to notice that the output voltage is kept in the range by the application without control.

VI. CONCLUSION

This paper analyses three different resonant network topologies by means of simple analytical equations based on simplifications.

Analyzing losses, behavior of the converter (as voltage source or current source), air-gap variation effects, load dependencies and current through the inductors, one of the topology is selected to meet with the application's specifications.

The resonant compensation topology selected is Series-Series where the capacitors are tuned with the leakage-inductance. From this, it is possible to achieve a constant voltage regardless of load and coupling factor as long as the resonance is maintained. To meet this, along with the air-gap range, constant leakage-inductance must be achieved.

For this purpose, fast eddy current 3D Finite Element simulations are carried out to obtain a design of an inductive coil that agrees with the design criterion developed in this paper. With this design, it is plausible to obtain an almost constant leakage-inductance, keeping the resonance frequency and the behavior of voltage source.

In order to validate the methodology described in this work, an IPT system is simulated and built. From the simulation and the experimental results, it is noticed that the converter can operate in open-loop keeping the output voltage within the range. Due to the inductive behavior of the system, it is possible to achieve the ZVS condition. The efficiency of the system is kept over 94% throughout the entire range of operation.

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